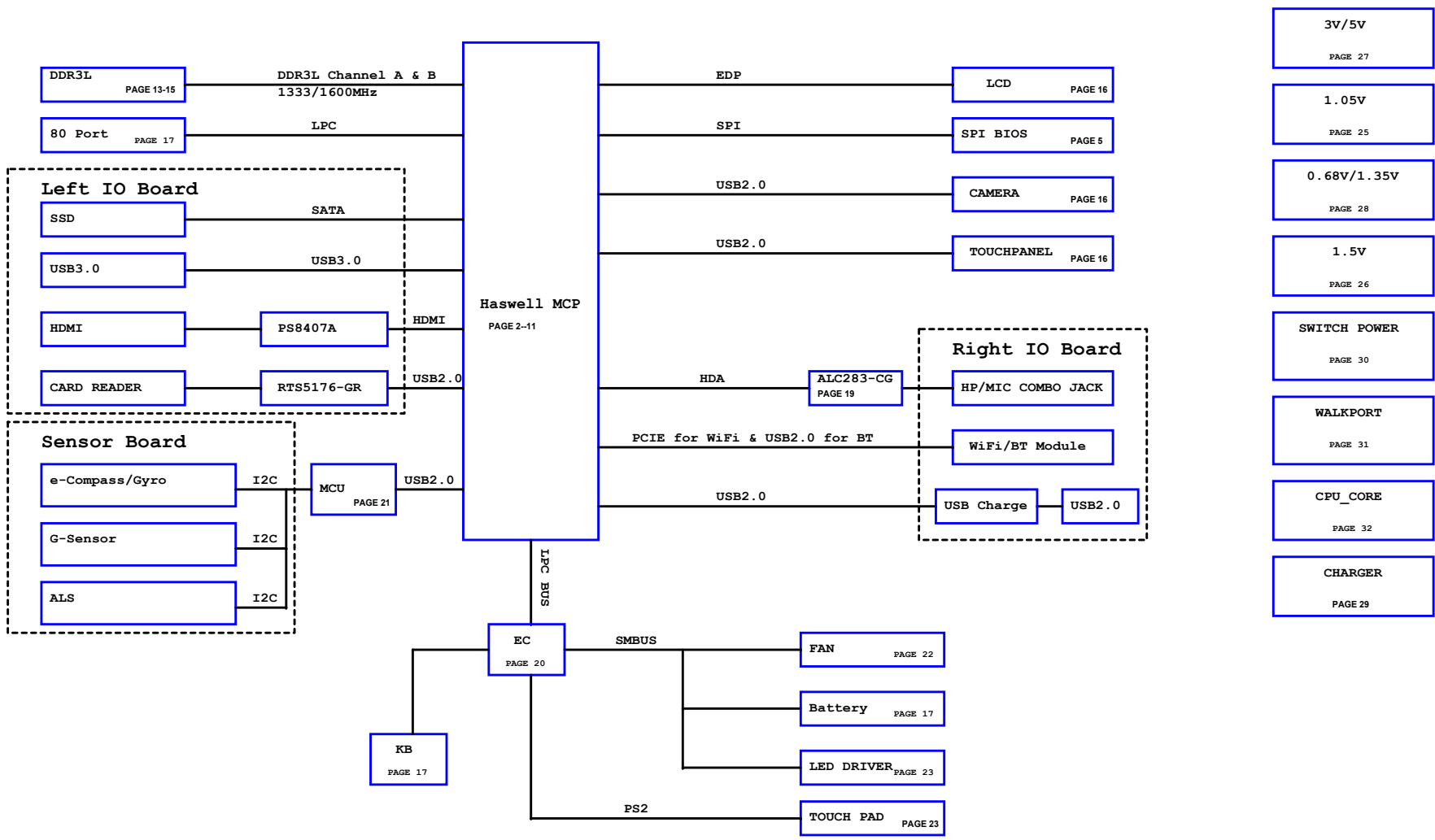
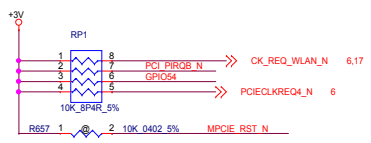
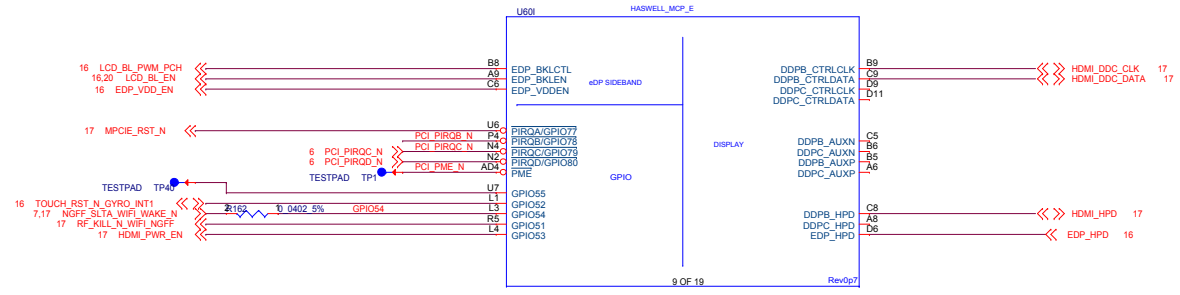
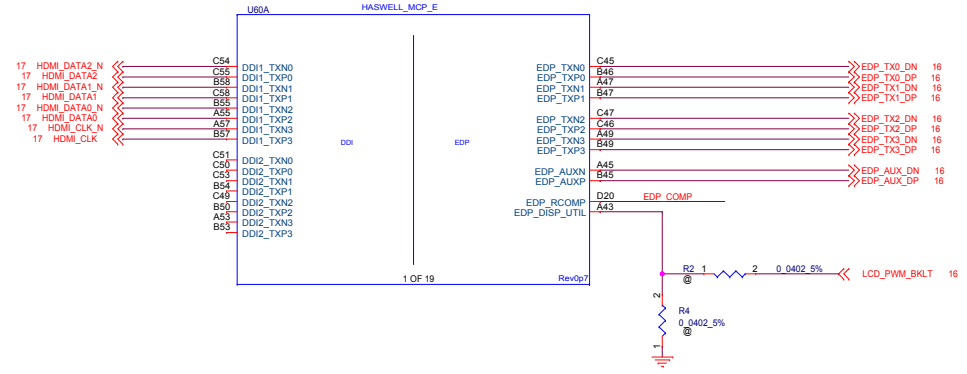
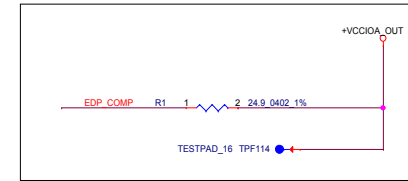


Kona SVT-NCS Schematic Block Diagram

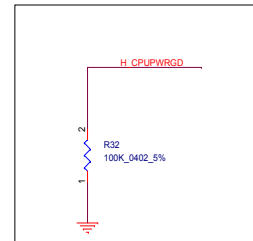
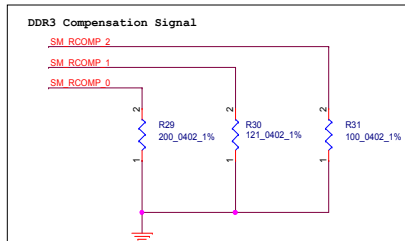
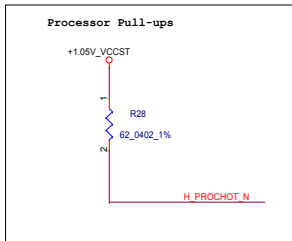
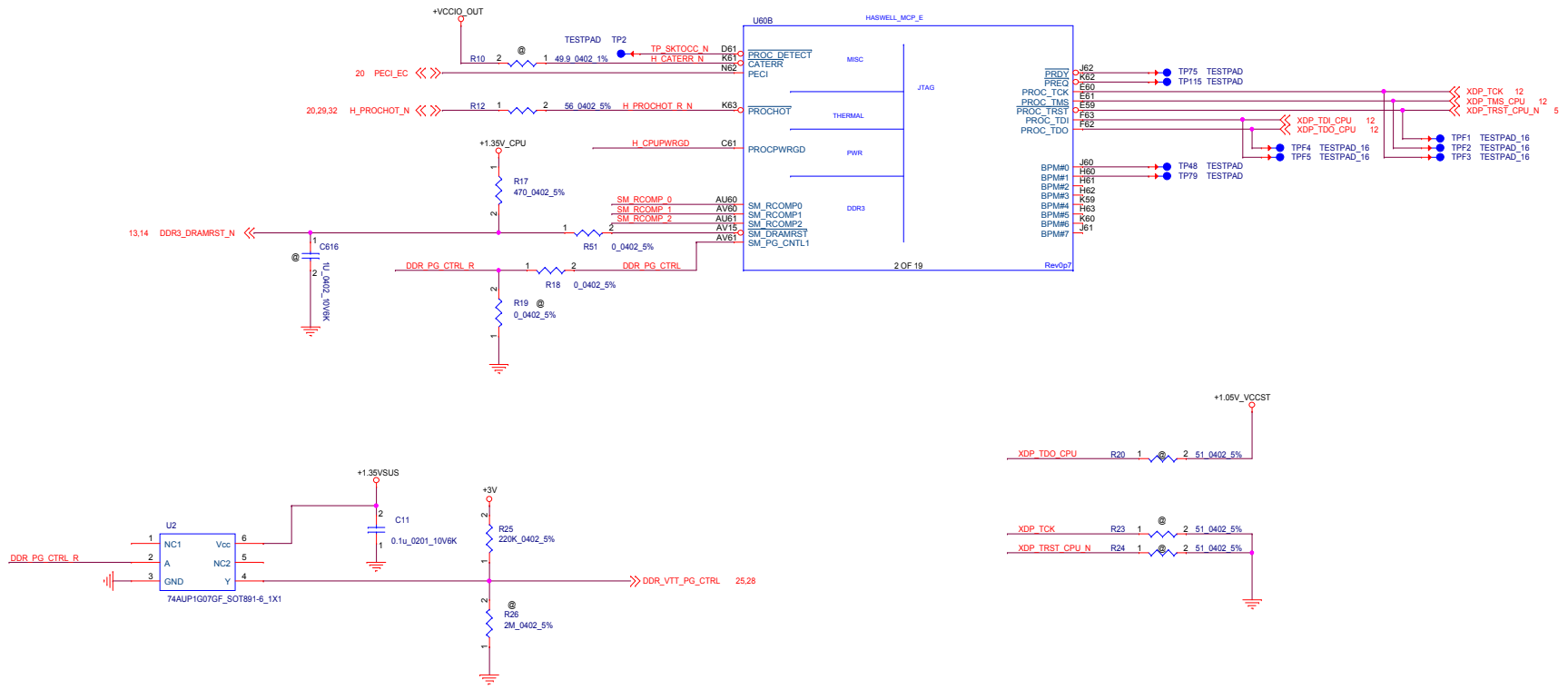


Haswell MCP (DDI,EDP)



| | | | |
|--|----------------------|-------------|--|
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| Title: Haswell MCP (DDI,EDP) | | | |
| Size C | Document Number Kona | Rev V1.0 | |
| Date: Tuesday, August 06, 2013 | Sheet 2 of 34 | | |
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Haswell MCP (MISC, THERMAL, JATG)



Haswell MCP (LPDDR3)

| U80C | | HASWELL_MCP_E | |
|----------------|-------------------------------------|-----------------|-------------------------------------|
| Left pins name | Option A/Option B/Option C/Option D | Right pins name | Option D/Option C/Option B/Option A |
| M_A_D00 | AH83 | SA_CLK0 | AU37 |
| M_A_D01 | AH82 | SA_CLK1 | AU38 |
| M_A_D02 | AK63 | SA_CLK2 | AU39 |
| M_A_D03 | AK62 | SA_CLK3 | AU40 |
| M_A_D04 | AH81 | SA_CLK4 | AU41 |
| M_A_D05 | AH80 | SA_CLK5 | AU42 |
| M_A_D06 | AK61 | SA_CLK6 | AU43 |
| M_A_D07 | AK60 | SA_CLK7 | AU44 |
| M_A_D08 | AM63 | SA_CLK8 | AU45 |
| M_A_D09 | AK59 | SA_CLK9 | AU46 |
| M_A_D10 | AK58 | SA_CLK10 | AU47 |
| M_A_D11 | AP63 | SA_CLK11 | AU48 |
| M_A_D12 | AP62 | SA_CLK12 | AU49 |
| M_A_D13 | AM61 | SA_CLK13 | AU50 |
| M_A_D14 | AP61 | SA_CLK14 | AU51 |
| M_A_D15 | AP60 | SA_CLK15 | AU52 |
| M_A_D16 | AP59 | SA_CLK16 | AU53 |
| M_A_D17 | AP58 | SA_CLK17 | AU54 |
| M_A_D18 | AM57 | SA_CLK18 | AU55 |
| M_A_D19 | AK57 | SA_CLK19 | AU56 |
| M_A_D20 | AL58 | SA_CLK20 | AU57 |
| M_A_D21 | AK56 | SA_CLK21 | AU58 |
| M_A_D22 | AP57 | SA_CLK22 | AU59 |
| M_A_D23 | AK55 | SA_CLK23 | AU60 |
| M_A_D24 | AM54 | SA_CLK24 | AU61 |
| M_A_D25 | AK54 | SA_CLK25 | AU62 |
| M_A_D26 | AK53 | SA_CLK26 | AU63 |
| M_A_D27 | AK52 | SA_CLK27 | AU64 |
| M_A_D28 | AK51 | SA_CLK28 | AU65 |
| M_A_D29 | AK50 | SA_CLK29 | AU66 |
| M_A_D30 | AK49 | SA_CLK30 | AU67 |
| M_A_D31 | AK48 | SA_CLK31 | AU68 |
| M_A_D32 | AK47 | SA_CLK32 | AU69 |
| M_A_D33 | AK46 | SA_CLK33 | AU70 |
| M_A_D34 | AK45 | SA_CLK34 | AU71 |
| M_A_D35 | AK44 | SA_CLK35 | AU72 |
| M_A_D36 | AK43 | SA_CLK36 | AU73 |
| M_A_D37 | AK42 | SA_CLK37 | AU74 |
| M_A_D38 | AK41 | SA_CLK38 | AU75 |
| M_A_D39 | AK40 | SA_CLK39 | AU76 |
| M_A_D40 | AK39 | SA_CLK40 | AU77 |
| M_A_D41 | AK38 | SA_CLK41 | AU78 |
| M_A_D42 | AK37 | SA_CLK42 | AU79 |
| M_A_D43 | AK36 | SA_CLK43 | AU80 |
| M_A_D44 | AK35 | SA_CLK44 | AU81 |
| M_A_D45 | AK34 | SA_CLK45 | AU82 |
| M_A_D46 | AK33 | SA_CLK46 | AU83 |
| M_A_D47 | AK32 | SA_CLK47 | AU84 |
| M_A_D48 | AK31 | SA_CLK48 | AU85 |
| M_A_D49 | AK30 | SA_CLK49 | AU86 |
| M_A_D50 | AK29 | SA_CLK50 | AU87 |
| M_A_D51 | AK28 | SA_CLK51 | AU88 |
| M_A_D52 | AK27 | SA_CLK52 | AU89 |
| M_A_D53 | AK26 | SA_CLK53 | AU90 |
| M_A_D54 | AK25 | SA_CLK54 | AU91 |
| M_A_D55 | AK24 | SA_CLK55 | AU92 |
| M_A_D56 | AK23 | SA_CLK56 | AU93 |
| M_A_D57 | AK22 | SA_CLK57 | AU94 |
| M_A_D58 | AK21 | SA_CLK58 | AU95 |
| M_A_D59 | AK20 | SA_CLK59 | AU96 |
| M_A_D60 | AK19 | SA_CLK60 | AU97 |
| M_A_D61 | AK18 | SA_CLK61 | AU98 |
| M_A_D62 | AK17 | SA_CLK62 | AU99 |
| M_A_D63 | AK16 | SA_CLK63 | AU100 |

| U80D | | HASWELL_MCP_E | |
|----------------|-------------------------------------|-----------------------------|-------------------------------------|
| Left pins name | Option A/Option B/Option C/Option D | Right pins name | Option D/Option C/Option B/Option A |
| M_B_D00 | AY31 | SA_D00/SB_D00/SB_D01/SB_D02 | AM38 |
| M_B_D01 | AW31 | SA_D01/SB_D01/SB_D02/SB_D03 | AM39 |
| M_B_D02 | AW29 | SA_D02/SB_D02/SB_D03/SB_D04 | AM40 |
| M_B_D03 | AW28 | SA_D03/SB_D03/SB_D04/SB_D05 | AM41 |
| M_B_D04 | AV31 | SA_D04/SB_D04/SB_D05/SB_D06 | AM42 |
| M_B_D05 | AV30 | SA_D05/SB_D05/SB_D06/SB_D07 | AM43 |
| M_B_D06 | AV29 | SA_D06/SB_D06/SB_D07/SB_D08 | AM44 |
| M_B_D07 | AU29 | SA_D07/SB_D07/SB_D08/SB_D09 | AM45 |
| M_B_D08 | AU28 | SA_D08/SB_D08/SB_D09/SB_D10 | AM46 |
| M_B_D09 | AW27 | SA_D09/SB_D09/SB_D10/SB_D11 | AM47 |
| M_B_D10 | AW26 | SA_D10/SB_D10/SB_D11/SB_D12 | AM48 |
| M_B_D11 | AV25 | SA_D11/SB_D11/SB_D12/SB_D13 | AM49 |
| M_B_D12 | AV24 | SA_D12/SB_D12/SB_D13/SB_D14 | AM50 |
| M_B_D13 | AU25 | SA_D13/SB_D13/SB_D14/SB_D15 | AM51 |
| M_B_D14 | AU24 | SA_D14/SB_D14/SB_D15/SB_D16 | AM52 |
| M_B_D15 | AW23 | SA_D15/SB_D15/SB_D16/SB_D17 | AM53 |
| M_B_D16 | AW22 | SA_D16/SB_D16/SB_D17/SB_D18 | AM54 |
| M_B_D17 | AV23 | SA_D17/SB_D17/SB_D18/SB_D19 | AM55 |
| M_B_D18 | AV22 | SA_D18/SB_D18/SB_D19/SB_D20 | AM56 |
| M_B_D19 | AW21 | SA_D19/SB_D19/SB_D20/SB_D21 | AM57 |
| M_B_D20 | AW20 | SA_D20/SB_D20/SB_D21/SB_D22 | AM58 |
| M_B_D21 | AV21 | SA_D21/SB_D21/SB_D22/SB_D23 | AM59 |
| M_B_D22 | AV20 | SA_D22/SB_D22/SB_D23/SB_D24 | AM60 |
| M_B_D23 | AW19 | SA_D23/SB_D23/SB_D24/SB_D25 | AM61 |
| M_B_D24 | AW18 | SA_D24/SB_D24/SB_D25/SB_D26 | AM62 |
| M_B_D25 | AV19 | SA_D25/SB_D25/SB_D26/SB_D27 | AM63 |
| M_B_D26 | AV18 | SA_D26/SB_D26/SB_D27/SB_D28 | AM64 |
| M_B_D27 | AW17 | SA_D27/SB_D27/SB_D28/SB_D29 | AM65 |
| M_B_D28 | AW16 | SA_D28/SB_D28/SB_D29/SB_D30 | AM66 |
| M_B_D29 | AV17 | SA_D29/SB_D29/SB_D30/SB_D31 | AM67 |
| M_B_D30 | AV16 | SA_D30/SB_D30/SB_D31/SB_D32 | AM68 |
| M_B_D31 | AW15 | SA_D31/SB_D31/SB_D32/SB_D33 | AM69 |
| M_B_D32 | AW14 | SA_D32/SB_D32/SB_D33/SB_D34 | AM70 |
| M_B_D33 | AV15 | SA_D33/SB_D33/SB_D34/SB_D35 | AM71 |
| M_B_D34 | AV14 | SA_D34/SB_D34/SB_D35/SB_D36 | AM72 |
| M_B_D35 | AW13 | SA_D35/SB_D35/SB_D36/SB_D37 | AM73 |
| M_B_D36 | AW12 | SA_D36/SB_D36/SB_D37/SB_D38 | AM74 |
| M_B_D37 | AV13 | SA_D37/SB_D37/SB_D38/SB_D39 | AM75 |
| M_B_D38 | AV12 | SA_D38/SB_D38/SB_D39/SB_D40 | AM76 |
| M_B_D39 | AW11 | SA_D39/SB_D39/SB_D40/SB_D41 | AM77 |
| M_B_D40 | AW10 | SA_D40/SB_D40/SB_D41/SB_D42 | AM78 |
| M_B_D41 | AV11 | SA_D41/SB_D41/SB_D42/SB_D43 | AM79 |
| M_B_D42 | AV10 | SA_D42/SB_D42/SB_D43/SB_D44 | AM80 |
| M_B_D43 | AW09 | SA_D43/SB_D43/SB_D44/SB_D45 | AM81 |
| M_B_D44 | AW08 | SA_D44/SB_D44/SB_D45/SB_D46 | AM82 |
| M_B_D45 | AV09 | SA_D45/SB_D45/SB_D46/SB_D47 | AM83 |
| M_B_D46 | AV08 | SA_D46/SB_D46/SB_D47/SB_D48 | AM84 |
| M_B_D47 | AW07 | SA_D47/SB_D47/SB_D48/SB_D49 | AM85 |
| M_B_D48 | AW06 | SA_D48/SB_D48/SB_D49/SB_D50 | AM86 |
| M_B_D49 | AV07 | SA_D49/SB_D49/SB_D50/SB_D51 | AM87 |
| M_B_D50 | AV06 | SA_D50/SB_D50/SB_D51/SB_D52 | AM88 |
| M_B_D51 | AW05 | SA_D51/SB_D51/SB_D52/SB_D53 | AM89 |
| M_B_D52 | AW04 | SA_D52/SB_D52/SB_D53/SB_D54 | AM90 |
| M_B_D53 | AV05 | SA_D53/SB_D53/SB_D54/SB_D55 | AM91 |
| M_B_D54 | AV04 | SA_D54/SB_D54/SB_D55/SB_D56 | AM92 |
| M_B_D55 | AW03 | SA_D55/SB_D55/SB_D56/SB_D57 | AM93 |
| M_B_D56 | AW02 | SA_D56/SB_D56/SB_D57/SB_D58 | AM94 |
| M_B_D57 | AV03 | SA_D57/SB_D57/SB_D58/SB_D59 | AM95 |
| M_B_D58 | AV02 | SA_D58/SB_D58/SB_D59/SB_D60 | AM96 |
| M_B_D59 | AW01 | SA_D59/SB_D59/SB_D60/SB_D61 | AM97 |
| M_B_D60 | AW00 | SA_D60/SB_D60/SB_D61/SB_D62 | AM98 |
| M_B_D61 | AV01 | SA_D61/SB_D61/SB_D62/SB_D63 | AM99 |
| M_B_D62 | AV00 | SA_D62/SB_D62/SB_D63/SB_D64 | AM100 |
| M_B_D63 | AW00 | SA_D63/SB_D63/SB_D64/SB_D65 | AM101 |

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Title: Haswell MCP(LPDDR3)

Size: Kona

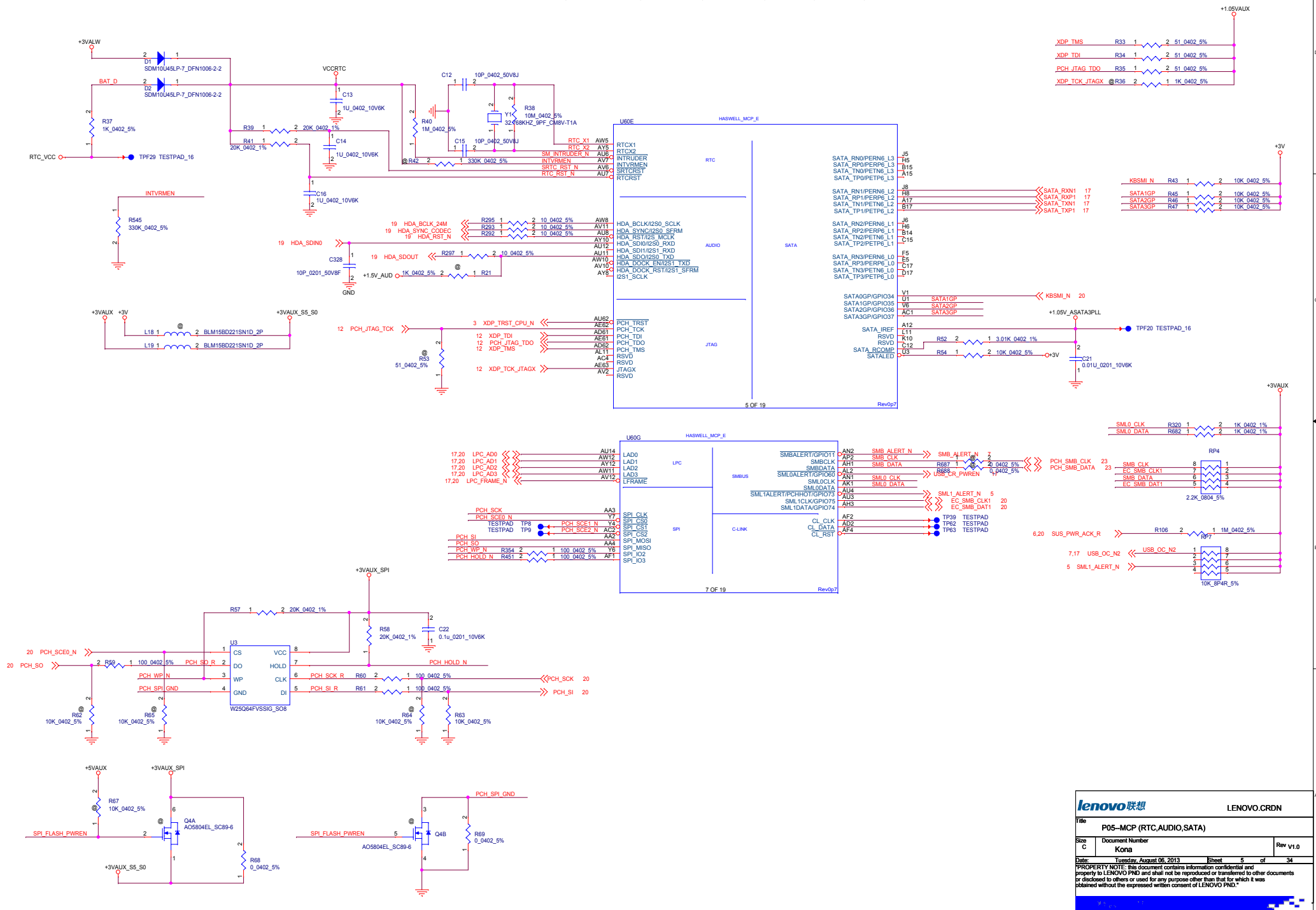
Date: Tuesday, August 06, 2013

Sheet: 4 of 34

Rev: V1.0

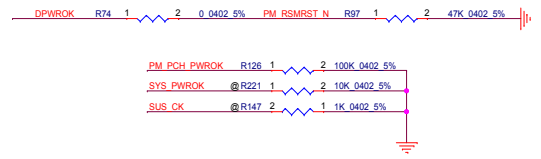
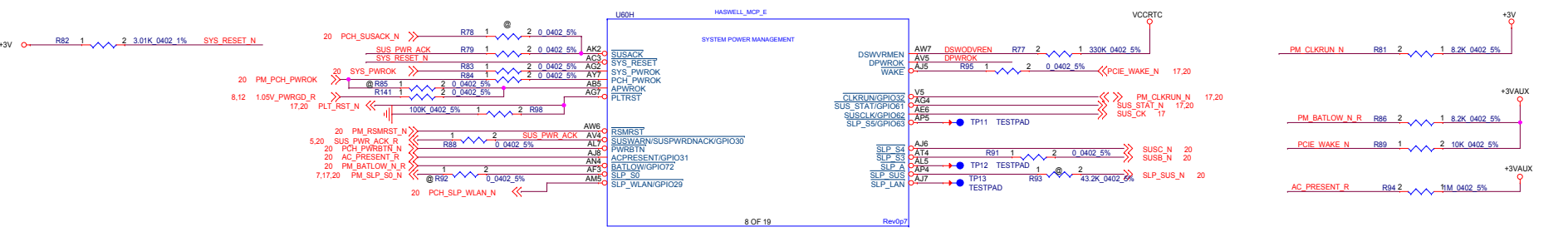
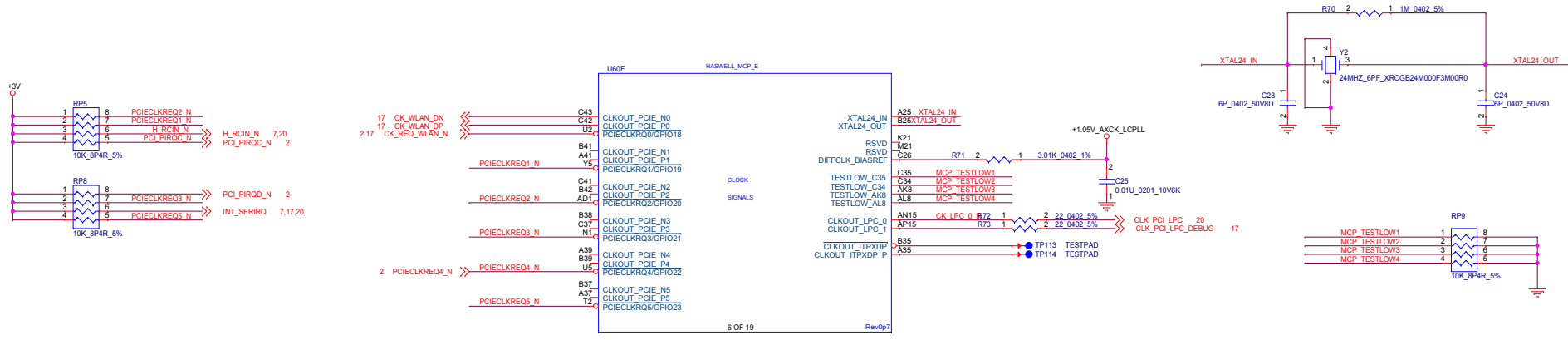
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Haswell MCP (RTC, AUDIO, JTAG, SATA, LPC, SPI, SMBUS)



| | | | |
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| Title: P05-MCP (RTC,AUDIO,SATA) | | | |
| Size: C | Document Number: Kona | Rev V1.0 | |
| Date: Tuesday, August 06, 2013 | Sheet: 5 of 34 | | |
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Haswell MCP (Clock, PM)



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File
Haswell MCP (Clock, PM)

Size
C

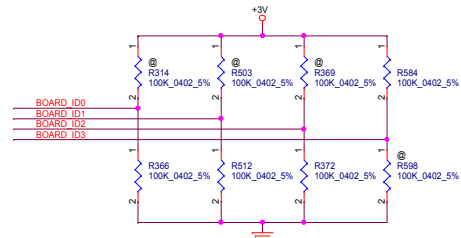
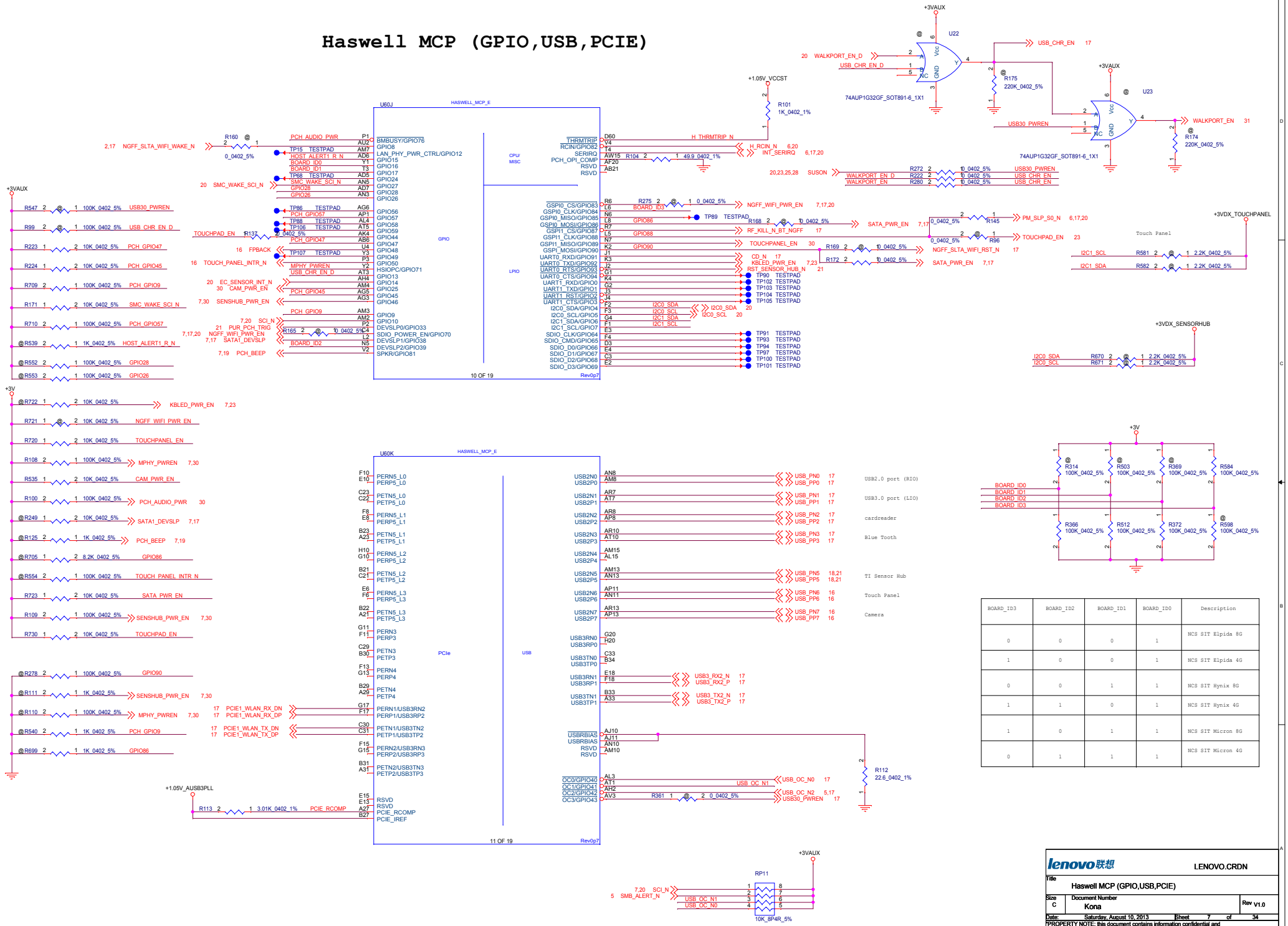
Document Number
Kona

Rev
V1.0

Date: Tuesday, August 06, 2013 **Sheet** 6 **of** 34

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Haswell MCP (GPIO,USB,PCIE)



| BOARD_ID3 | BOARD_ID2 | BOARD_ID1 | BOARD_ID0 | Description |
|-----------|-----------|-----------|-----------|-------------------|
| 0 | 0 | 0 | 1 | MCS SIT Elpida 8G |
| 1 | 0 | 0 | 1 | MCS SIT Elpida 4G |
| 0 | 0 | 1 | 1 | MCS SIT Hynix 8G |
| 1 | 1 | 0 | 1 | MCS SIT Hynix 4G |
| 1 | 0 | 1 | 1 | MCS SIT Micron 8G |
| 0 | 1 | 1 | 1 | MCS SIT Micron 4G |

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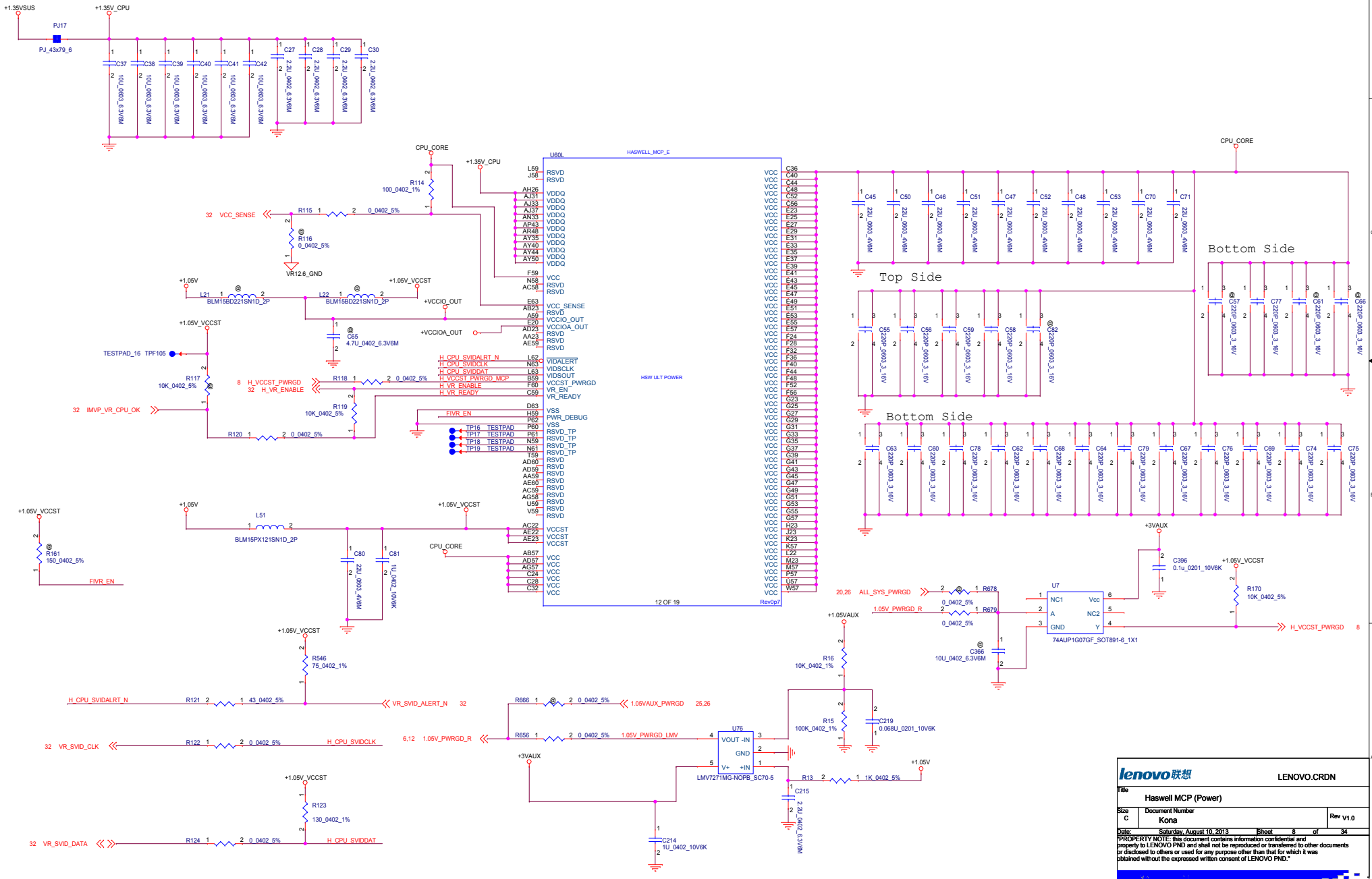
File: **Haswell MCP (GPIO,USB,PCIE)**

Size: **C** Document Number: **Kona** Rev: **V1.0**

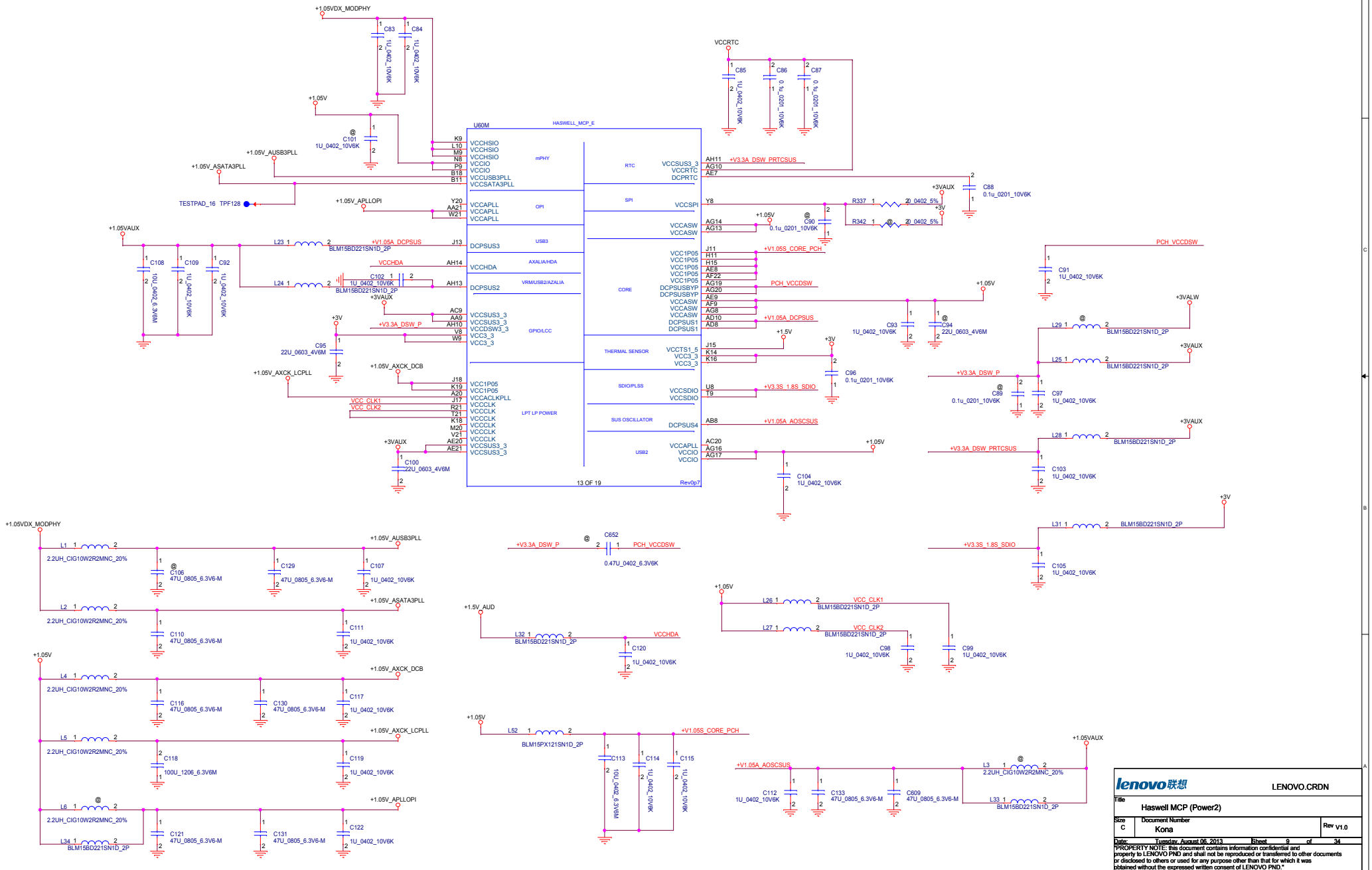
Date: **Saturday, August 10, 2013** Sheet: **7** of **34**


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Haswell MCP (Power)



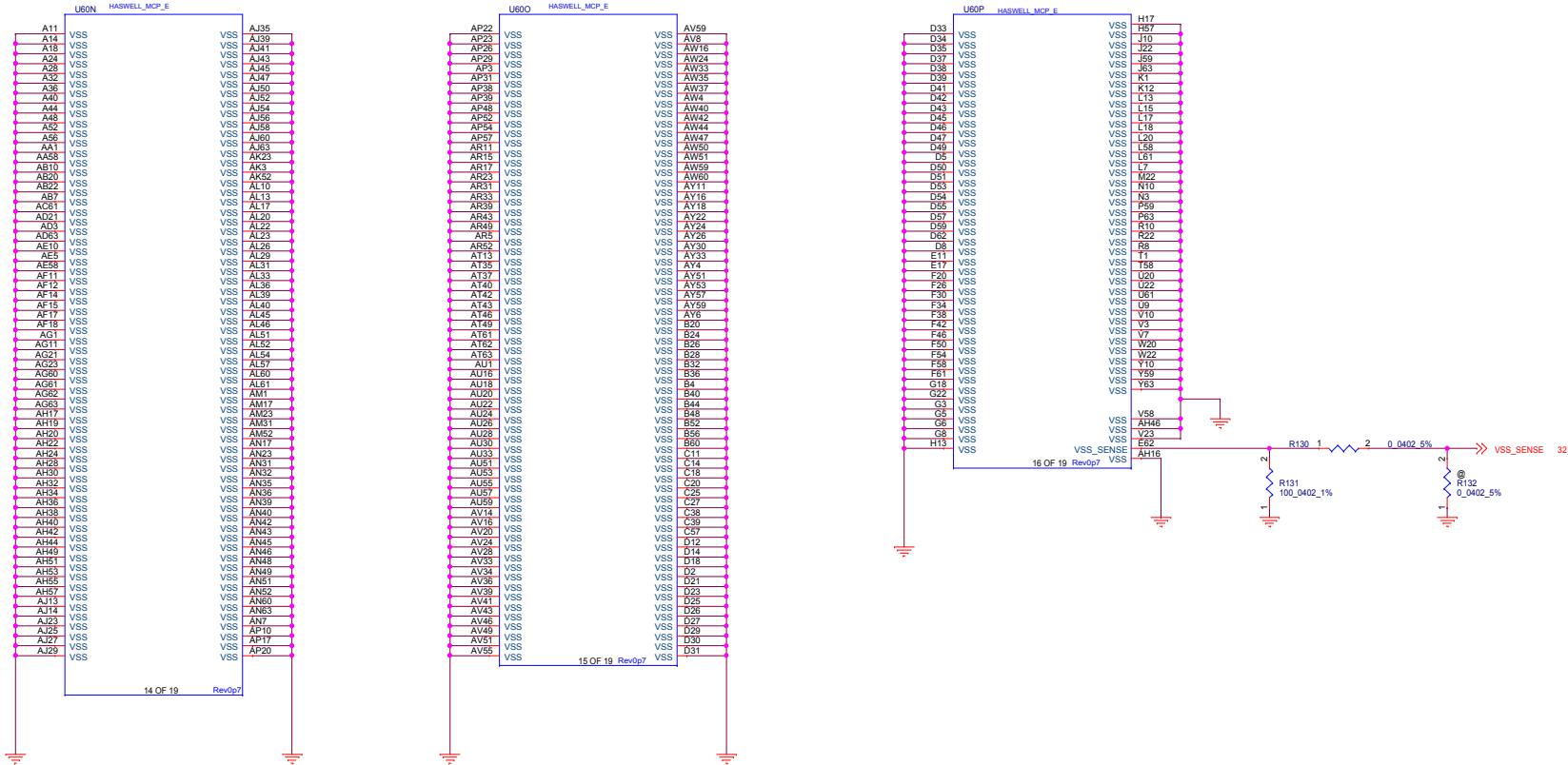
Haswell MCP (Power2)

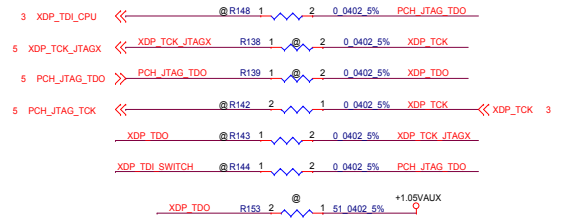
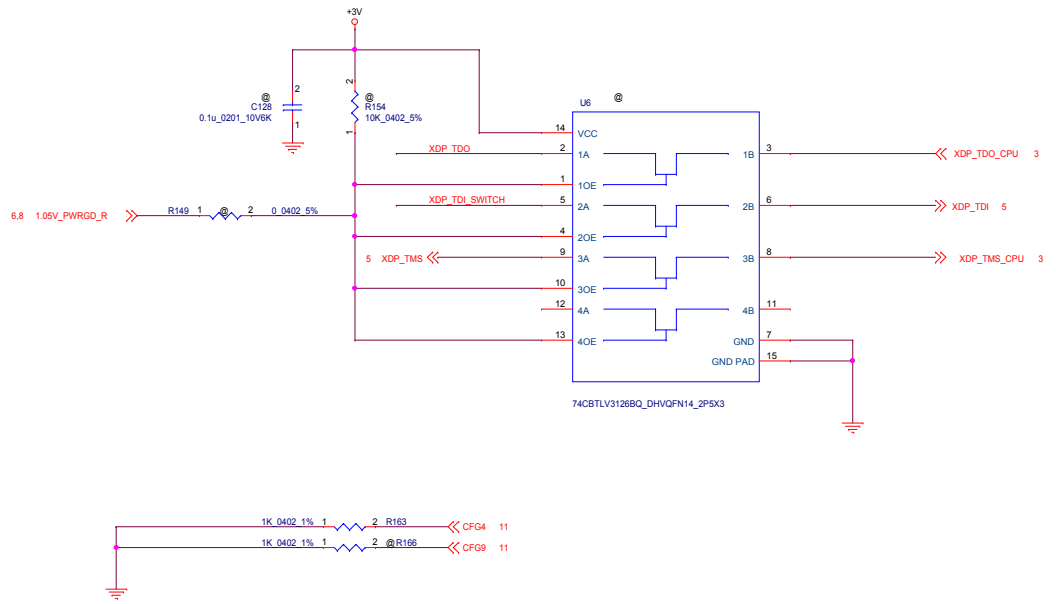



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| Haswell MCP (Power2) | |
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| Date: Tuesday, August 06, 2013 Sheet 9 of 34 | |
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Haswell MCP (VSS)





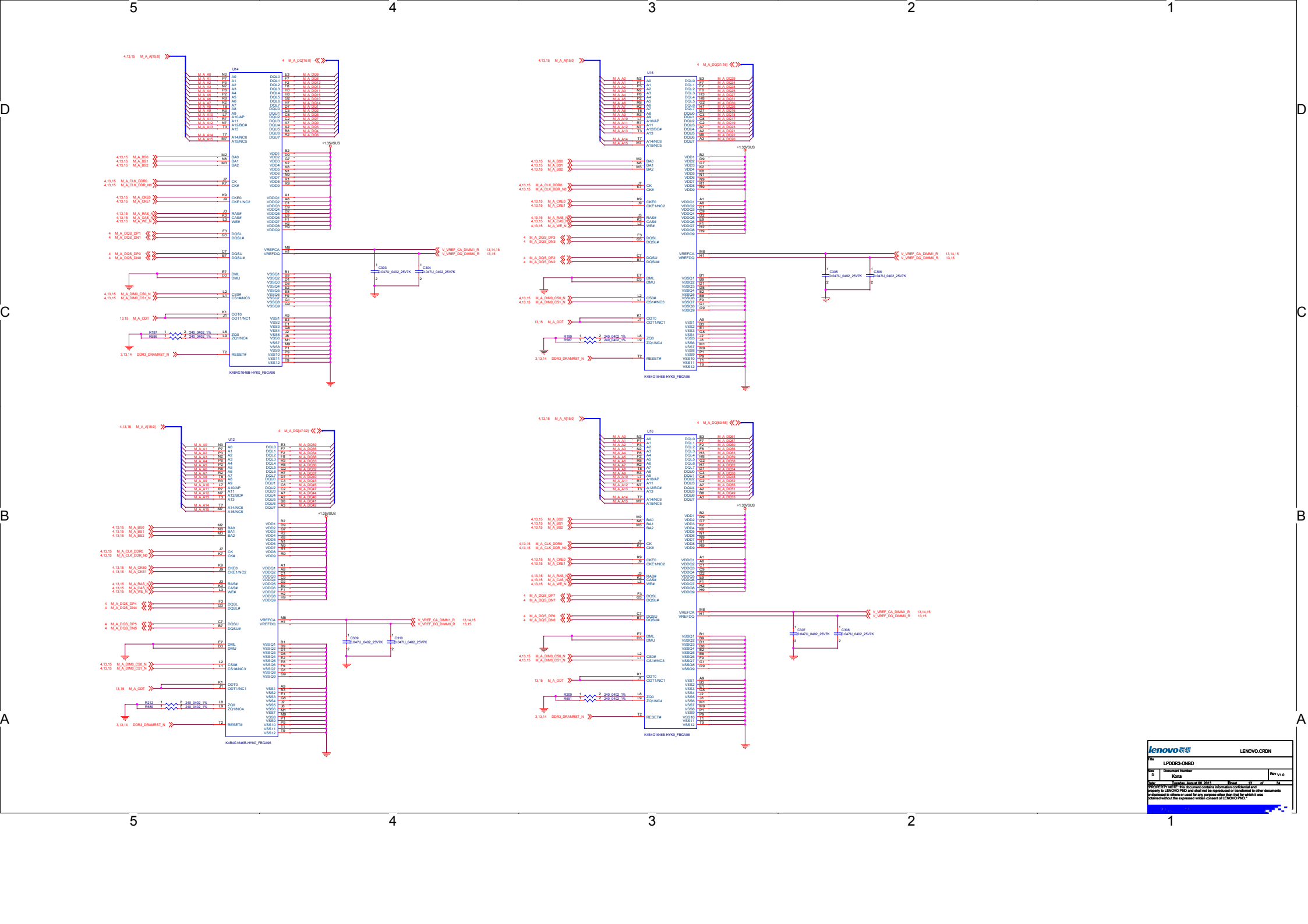
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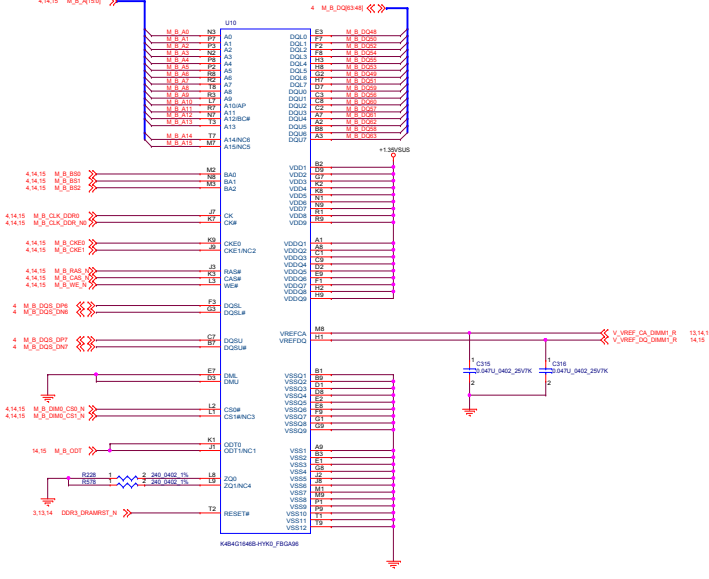
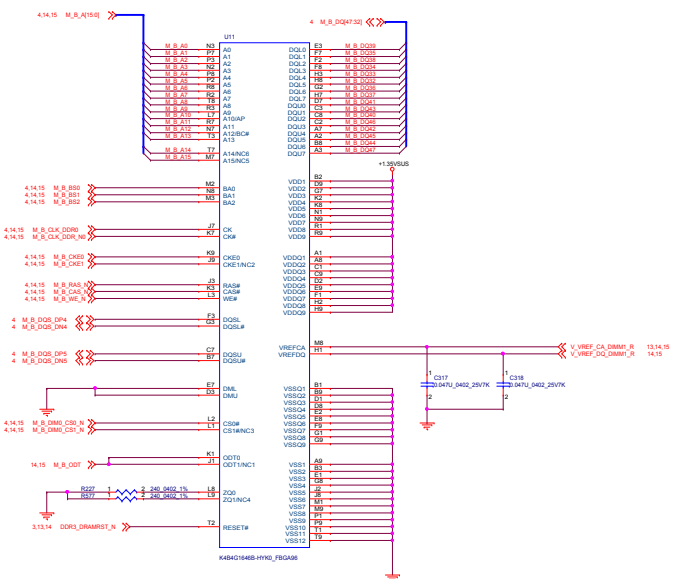
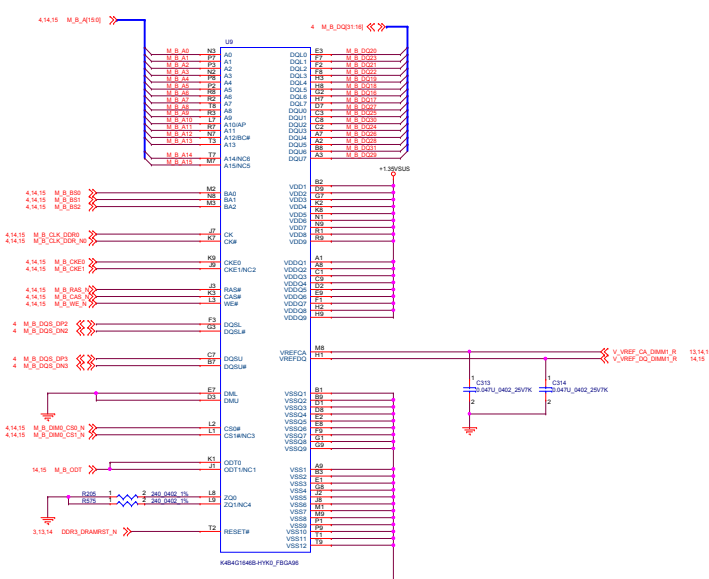
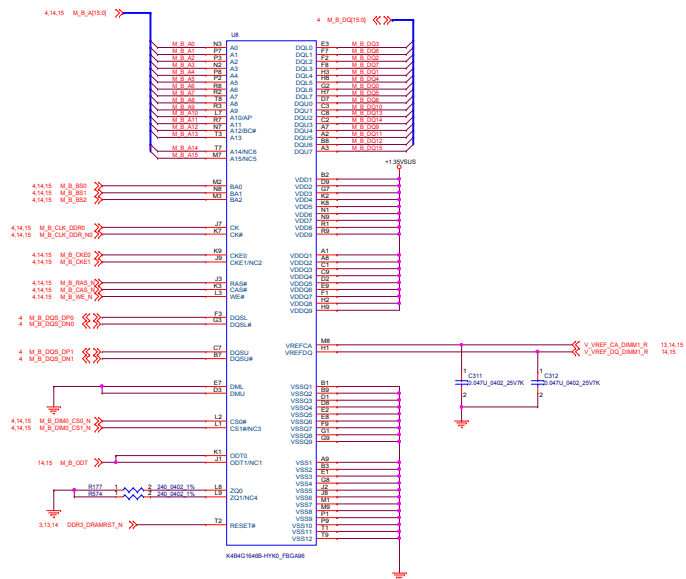
Title: XDP CONN

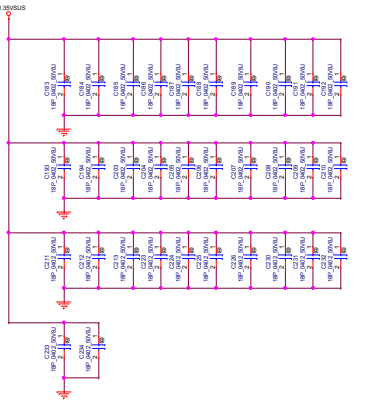
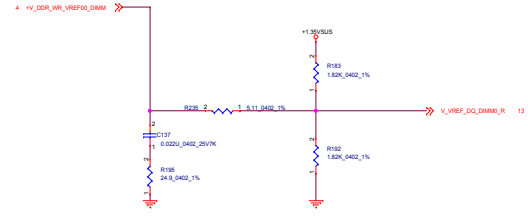
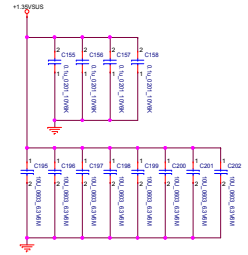
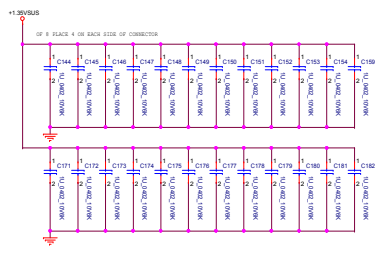
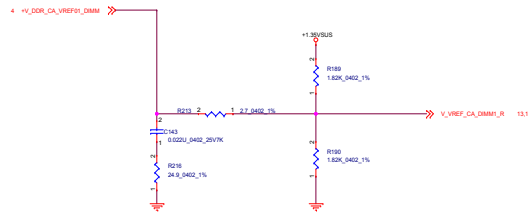
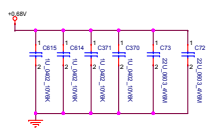
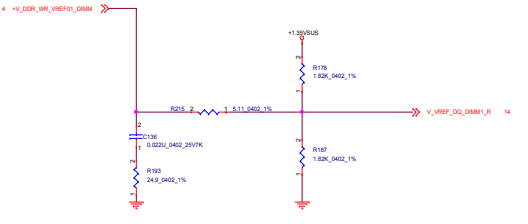
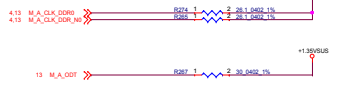
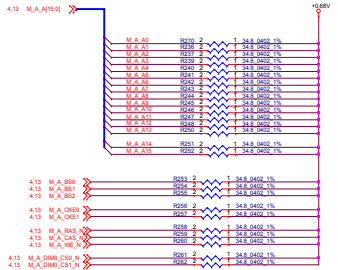
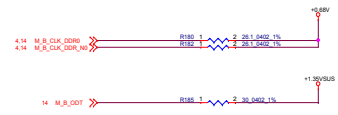
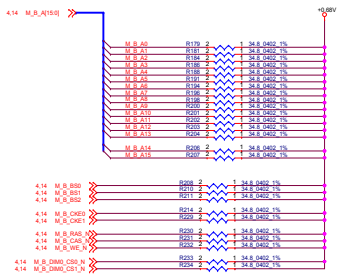
| | | |
|--------|----------------------|----------|
| Size C | Document Number Kona | Rev V1.0 |
|--------|----------------------|----------|

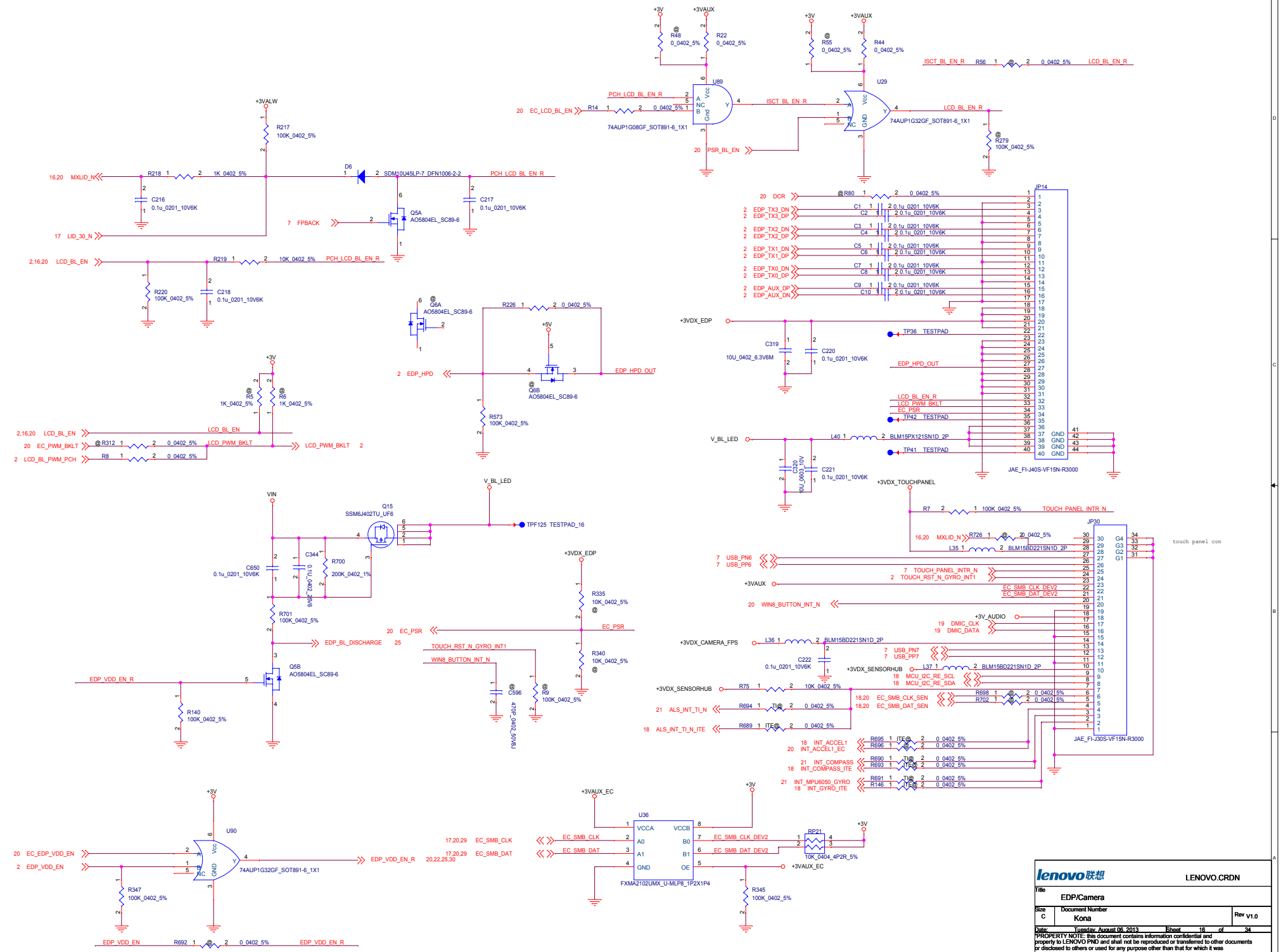
Date: Tuesday, August 06, 2013 Sheet 12 of 34

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Title: EDP/Camera

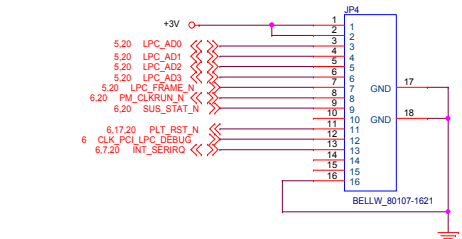
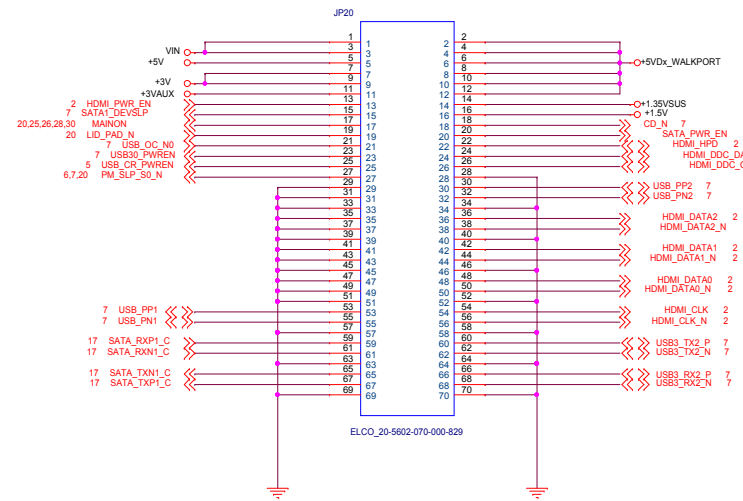
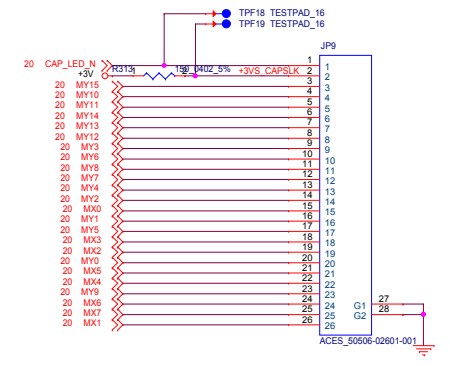
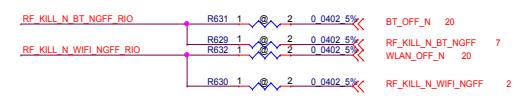
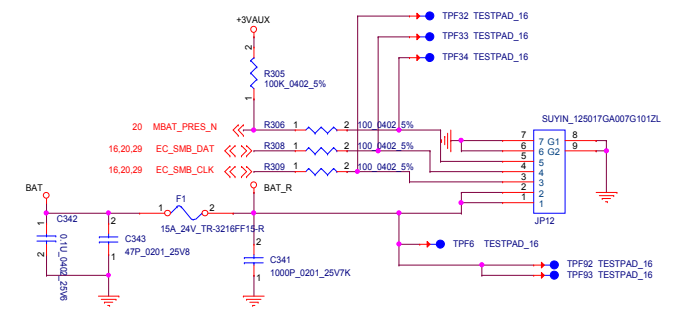
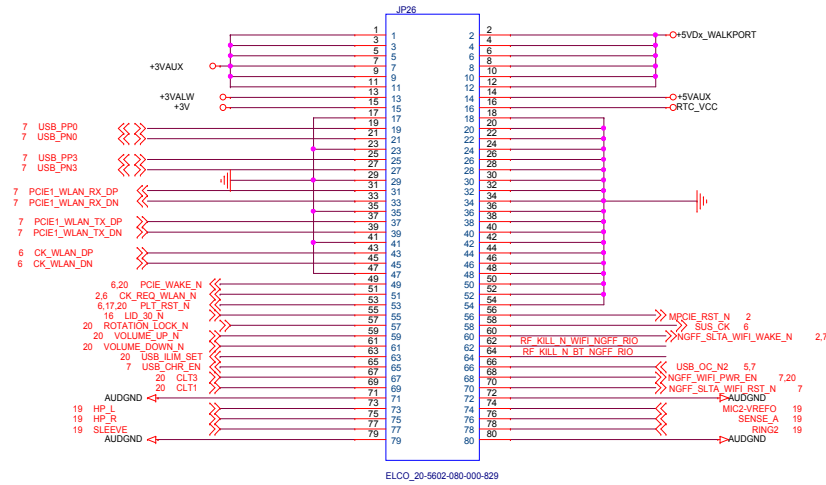
Size: C

Sheet: 16 of 34

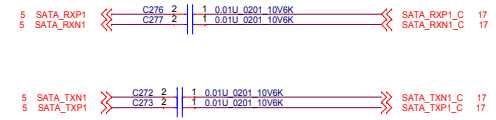
Date: Tuesday, August 06, 2013

Rev: V1.0

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删除了, CLK_REF14_SIO: CLK_PCI_SIO: PCH_DRQ#0



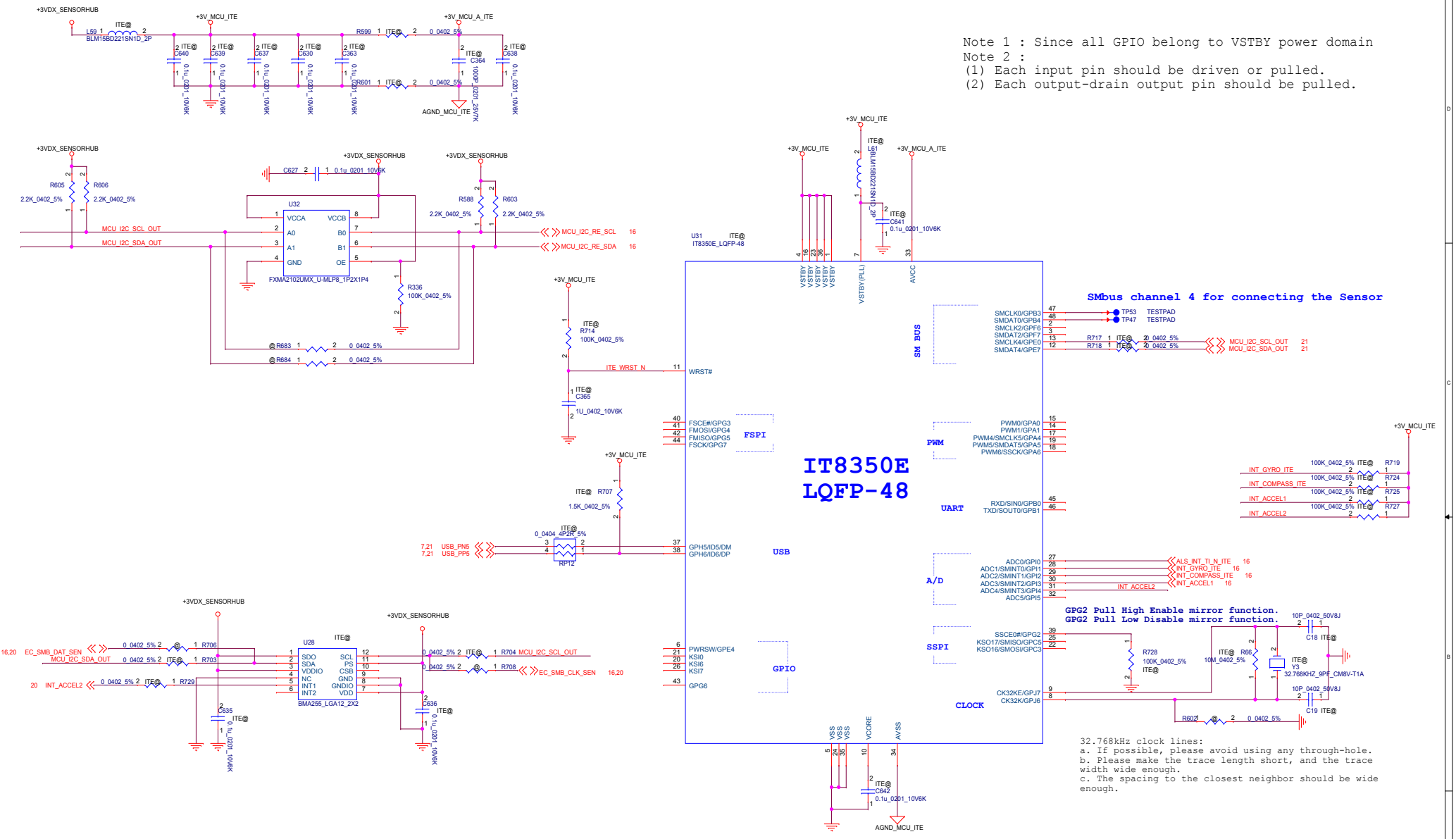
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File Connector (BTB,Bat,LPC,KB)

| | | |
|------|-----------------|------|
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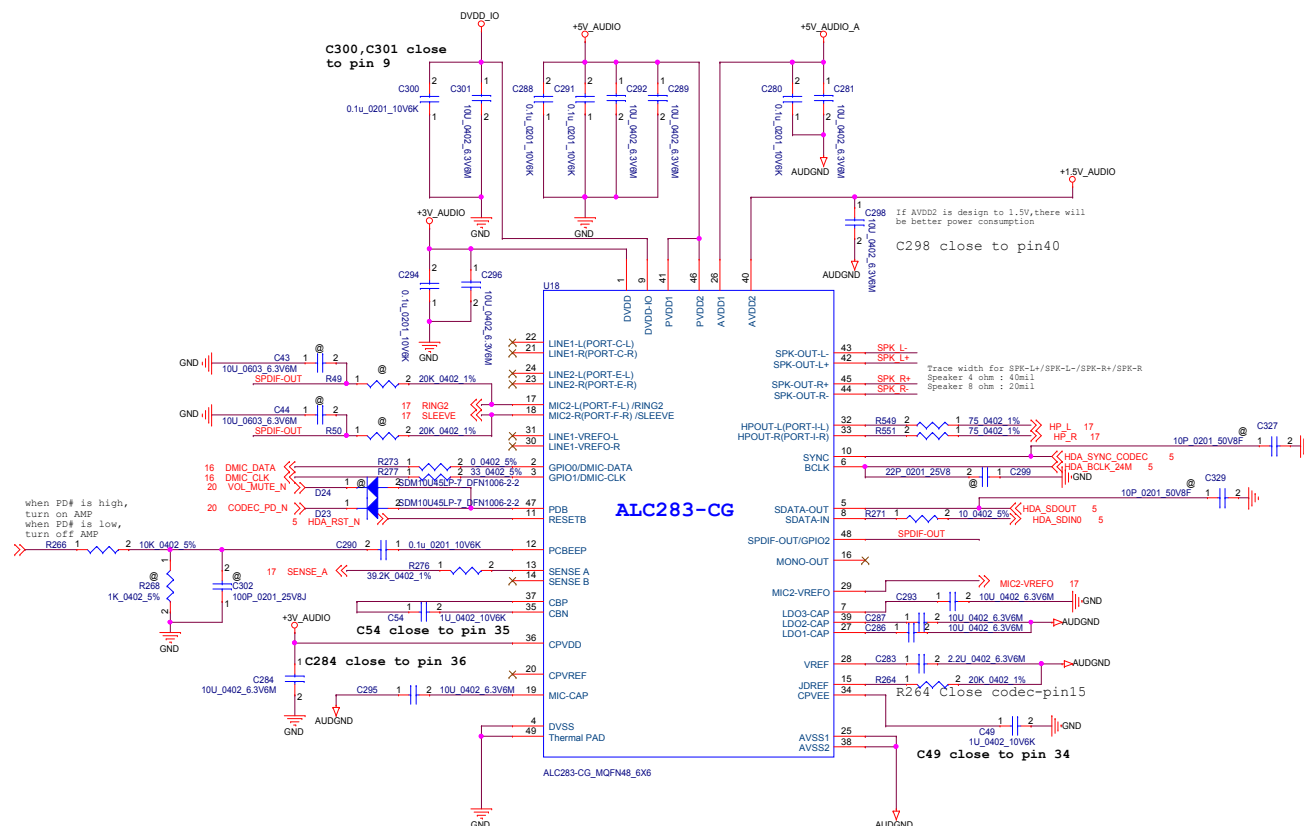


Note 1 : Since all GPIO belong to VSTBY power domain
 Note 2 :
 (1) Each input pin should be driven or pulled.
 (2) Each output-drain output pin should be pulled.

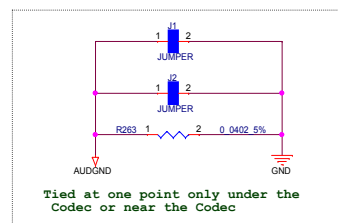
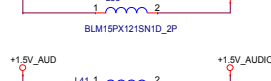
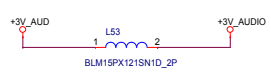
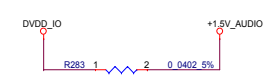
SMbus channel 4 for connecting the Sensor

GP2 Pull High Enable mirror function.
 GP2 Pull Low Disable mirror function.

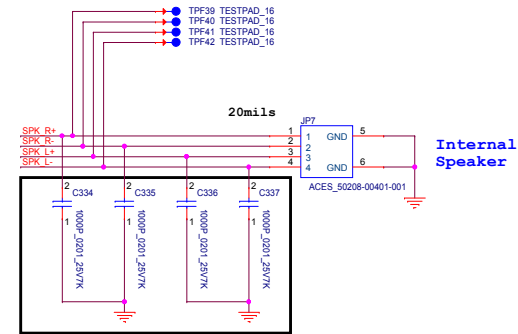
32.768kHz clock lines:
 a. If possible, please avoid using any through-hole.
 b. Please make the trace length short, and the trace width wide enough.
 c. The spacing to the closest neighbor should be wide enough.



Trace width for SPK-L/SPK-R/SPK-L-/SPK-R Speaker 4 ohm : 40mil
Speaker 8 ohm : 20mil

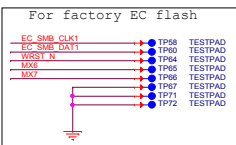
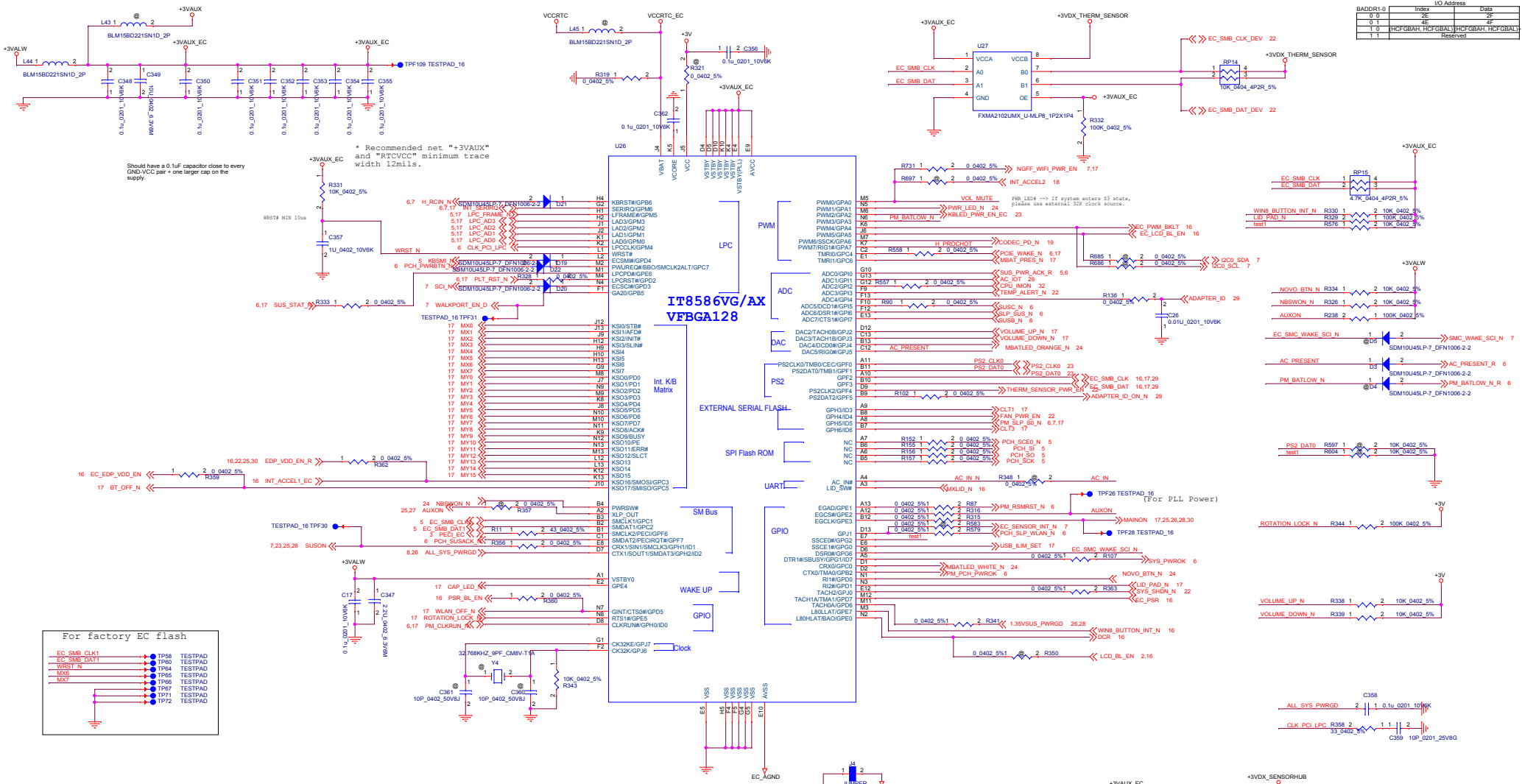


when PD# is high, turn on AMP
when PD# is low, turn off AMP



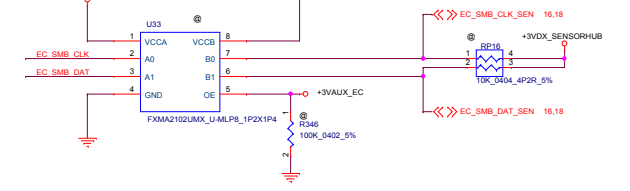
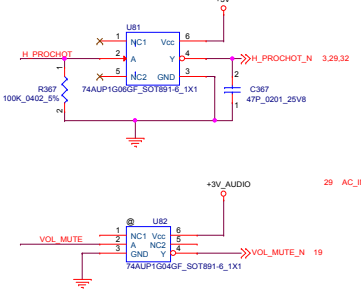
Near to device side U18

| BADDR1:0 | Index | IO Address | Data |
|----------|-------|--|----------|
| 0 | 0 | 2E | 2F |
| 0 | 1 | 4E | 4F |
| 1 | 0 | HCFC0BAH, HCFC0B4H, HCFC0BAH, HCFC0B4H | Reserved |
| 1 | 1 | Reserved | Reserved |



Note 1 : Since all GPIO belong to VSTBY power domain, and there are some special considerations below:
 (1) If it is output to external VCC derived power domain, this signal should be isolated by a diode such as KBRST# and GA20.
 (2) If it is input from external VCC derived power domain circuit, this external circuit must consider not to float the GPIO input.

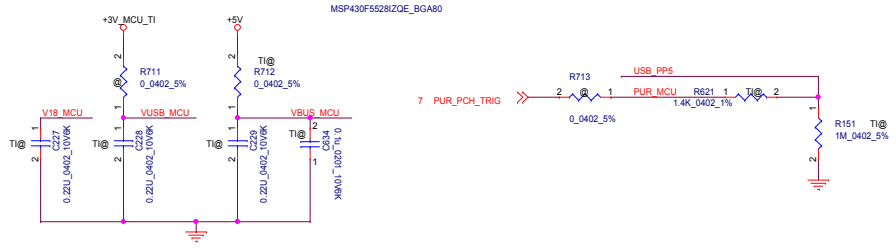
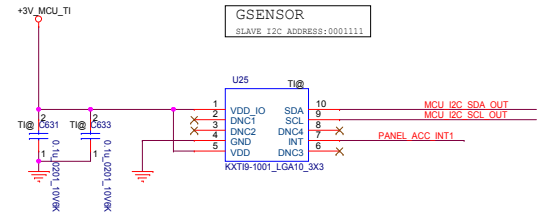
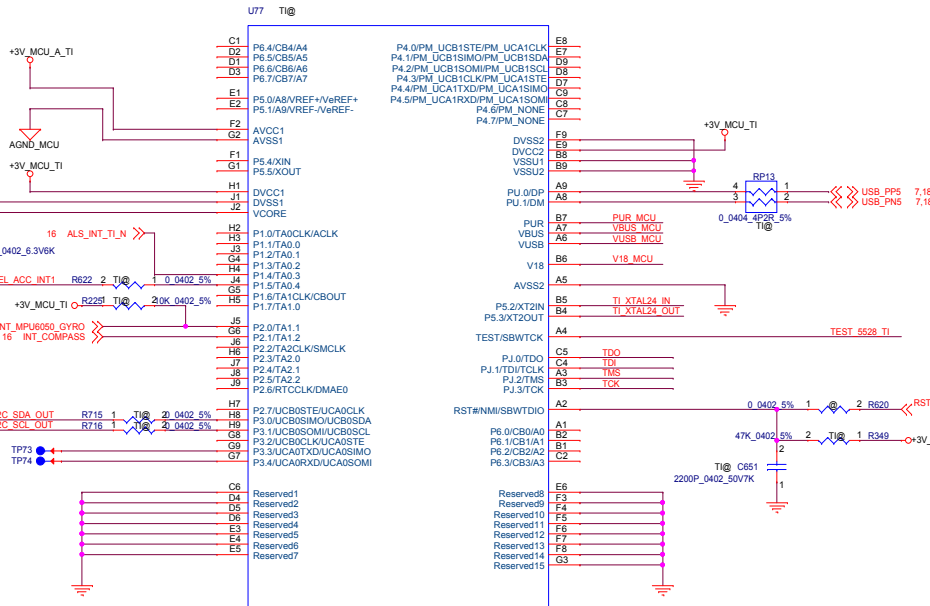
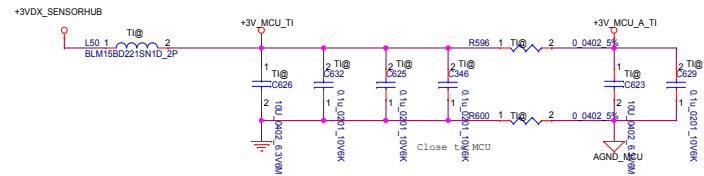
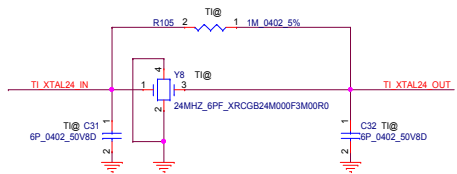
- Note 2 :
- Each input pin should be driven or pulled.
 - Each output-drain output pin should be pulled.



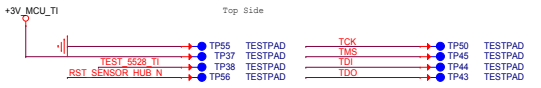
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MCU JTAG



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File: SENSOR (TI)

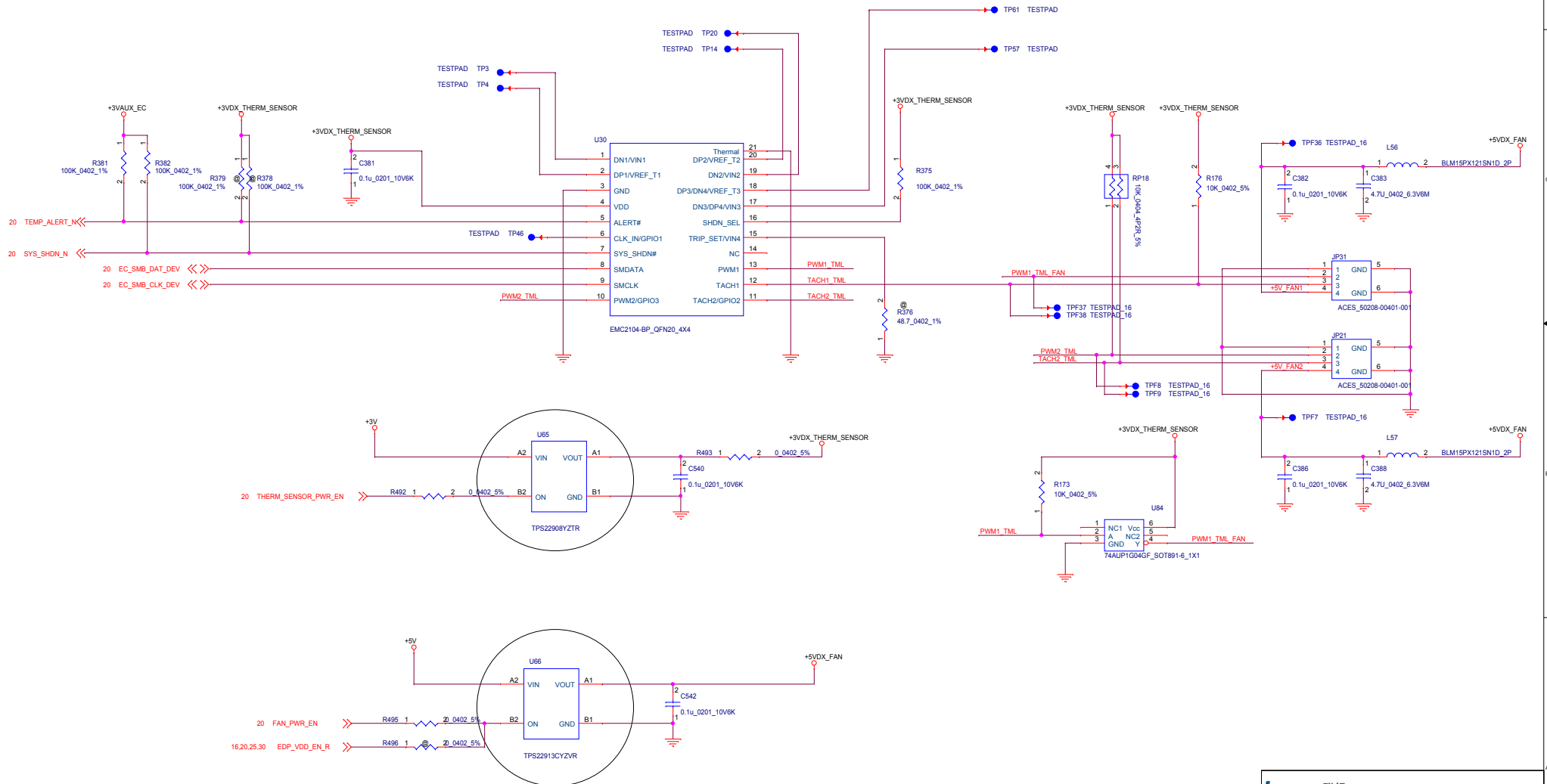
Size: C

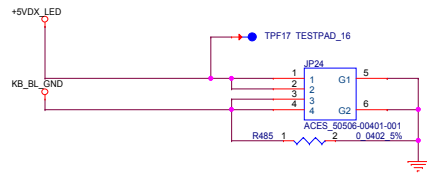
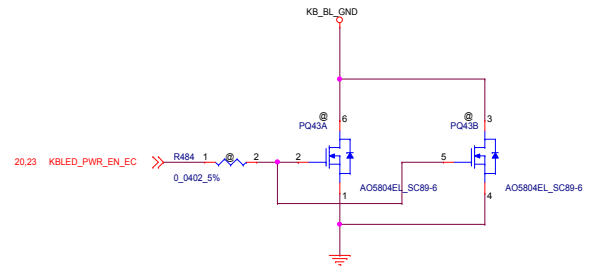
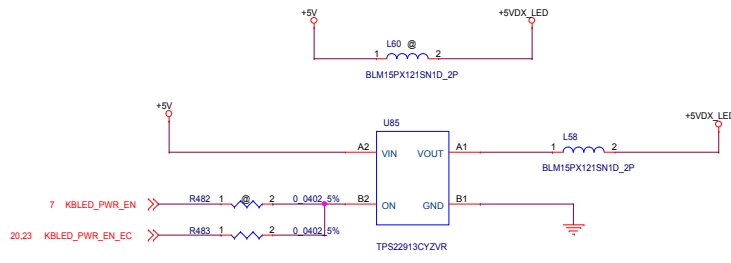
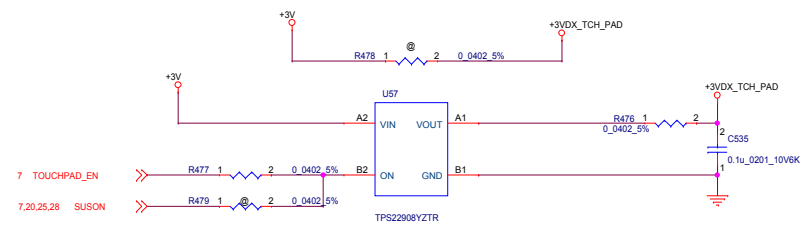
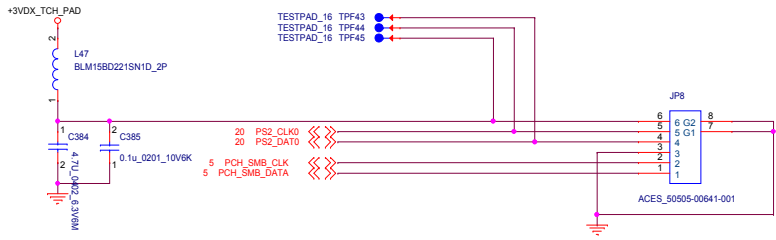
Document Number: Kona

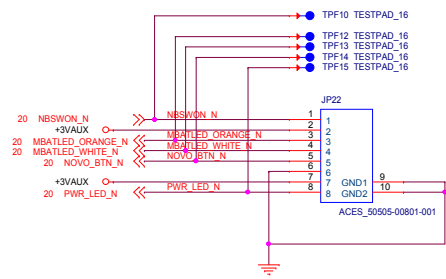
Rev: V1.0

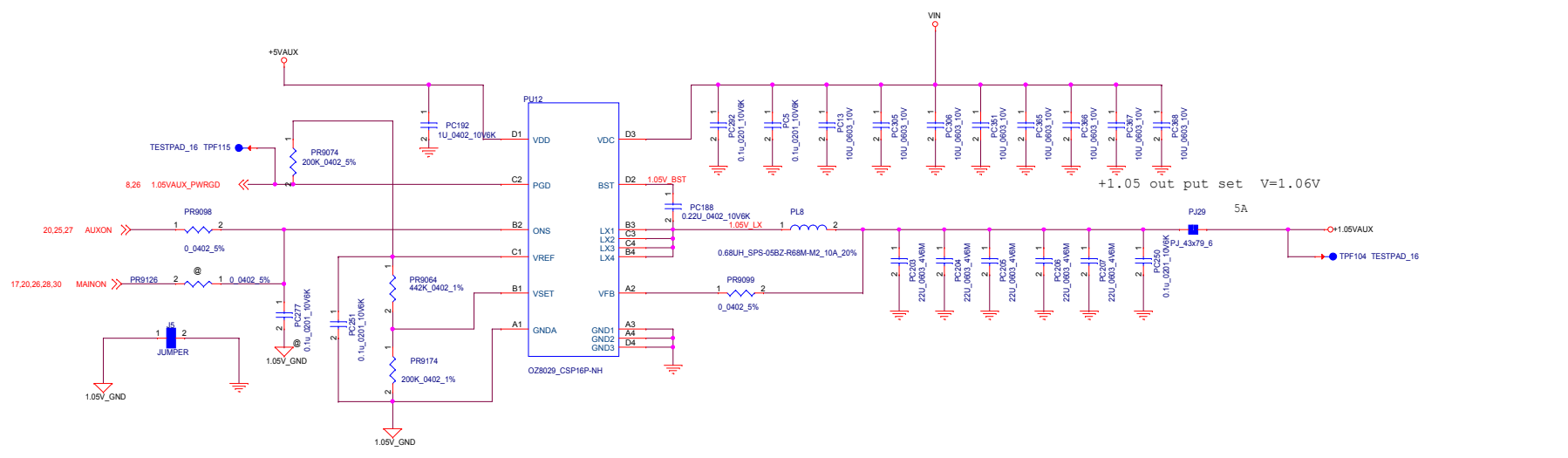
Date: Friday, August 09, 2013 Sheet: 21 of 34

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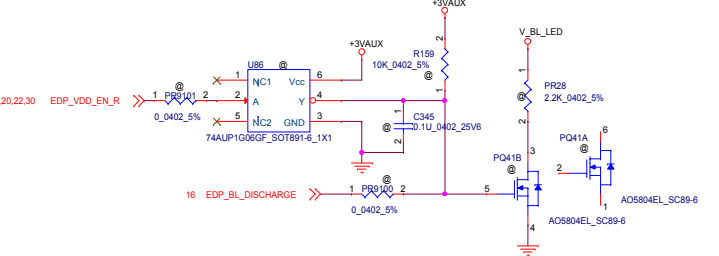
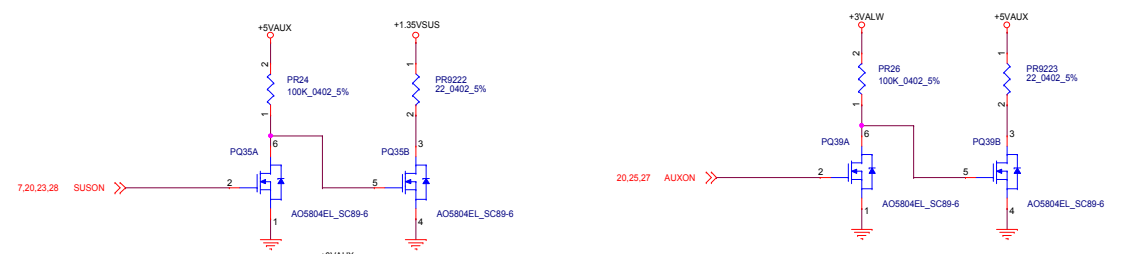
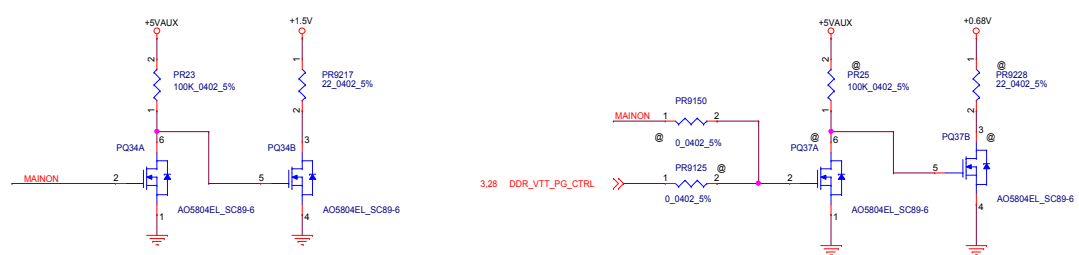




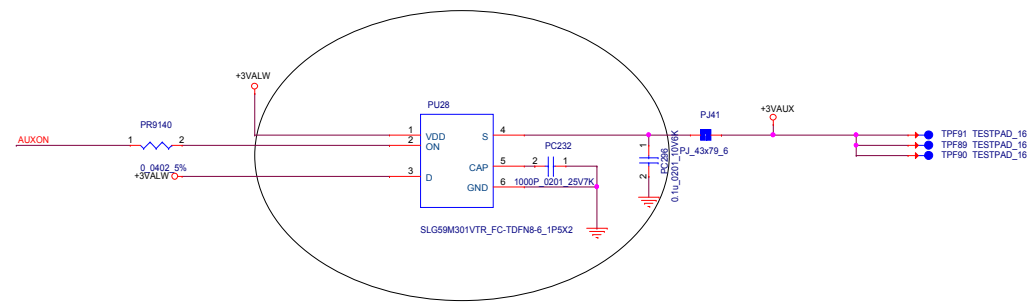
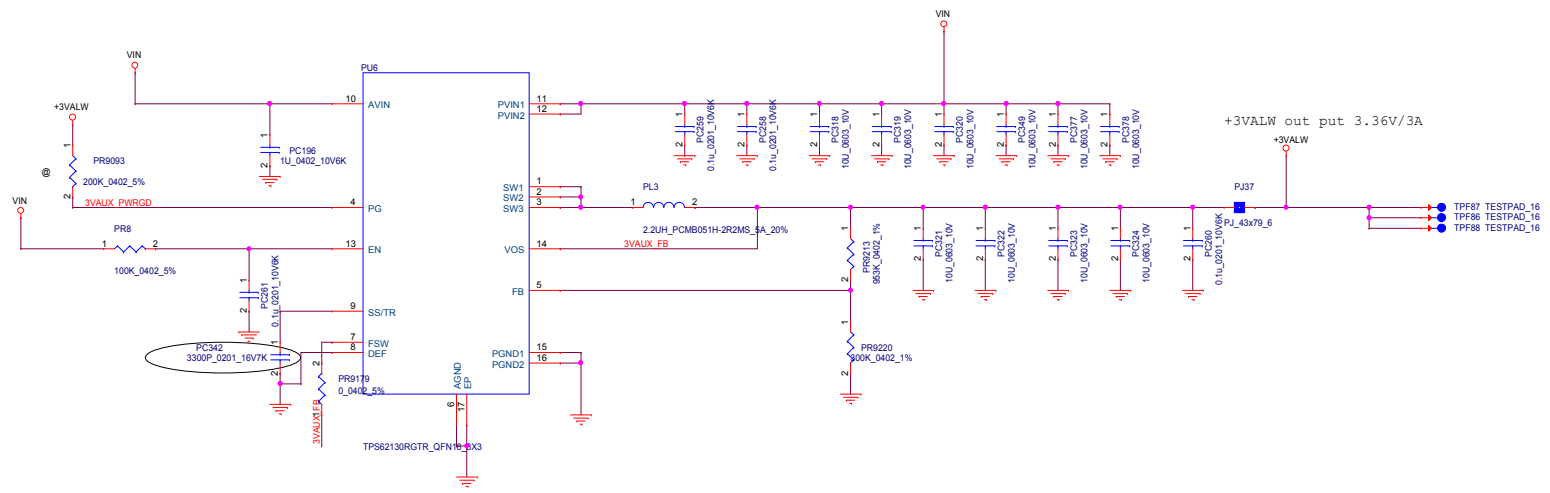
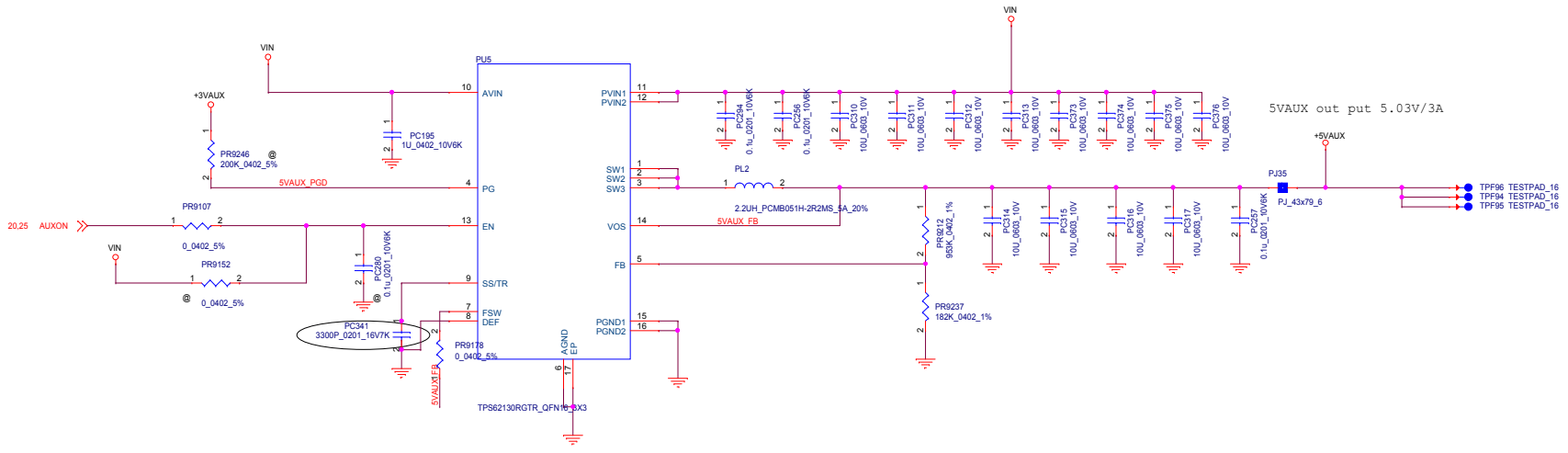




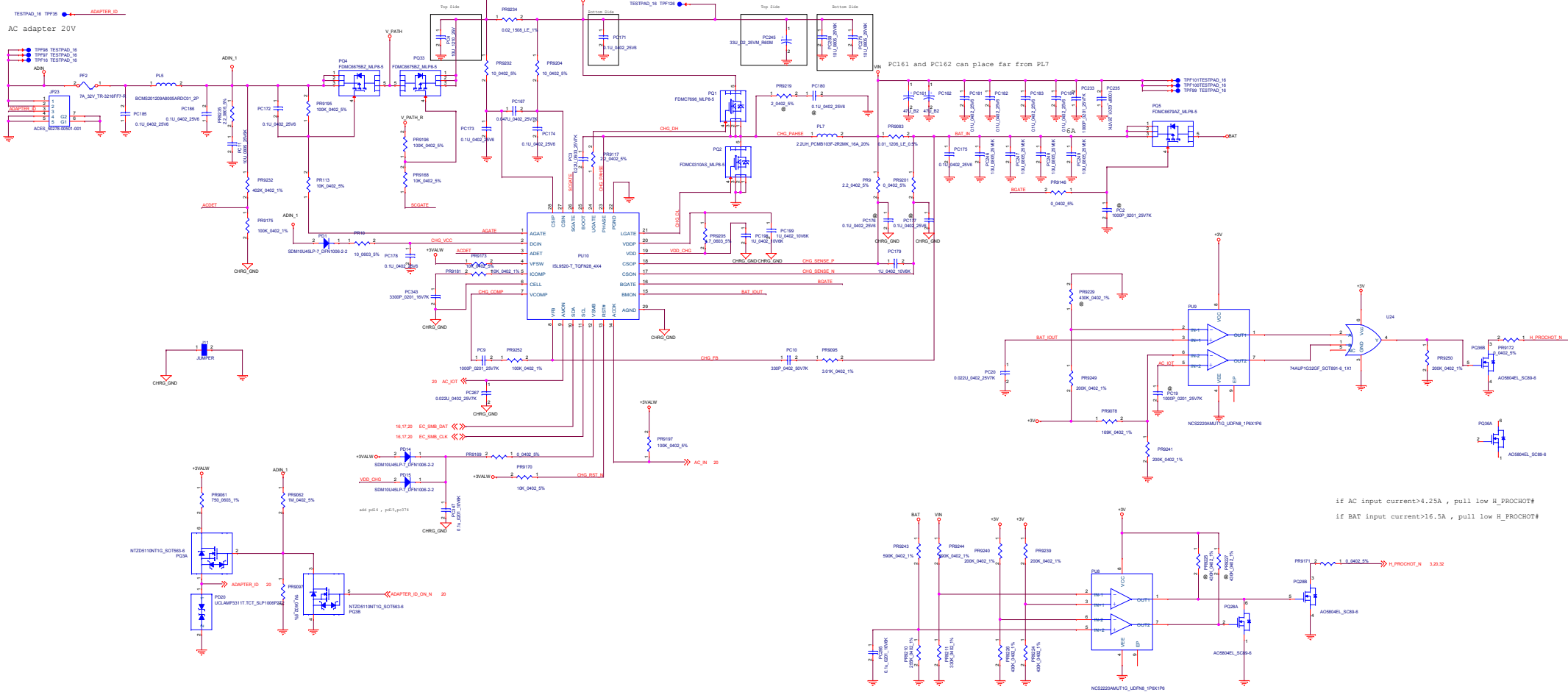
+1.05 out put set V=1.06V



| | | |
|-------|--------------------------|----------------|
| Title | | |
| 1005V | | |
| Size | Document Number | Rev |
| C | Kona | V1.0 |
| Date: | Tuesday, August 06, 2013 | Sheet 25 of 34 |



| | | |
|-----------------|--------------------------|----------------|
| Title | | |
| 5VALX3VALX3VALW | | |
| Size | Document Number | Rev |
| C | Kona | V1.0 |
| Date: | Tuesday, August 06, 2013 | Sheet 27 of 34 |



AC adapter 20V

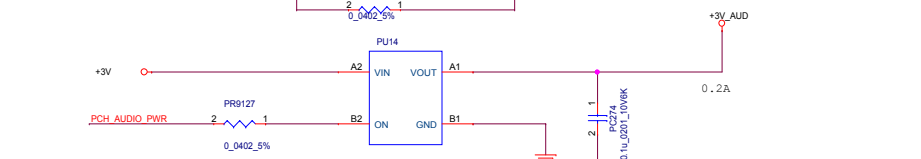
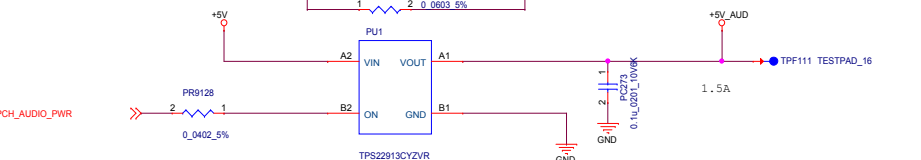
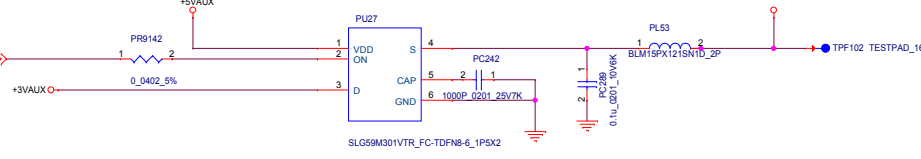
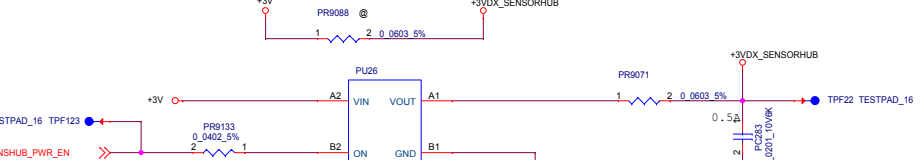
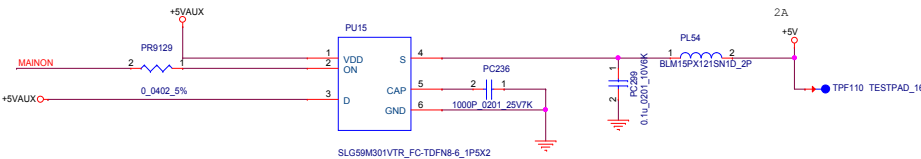
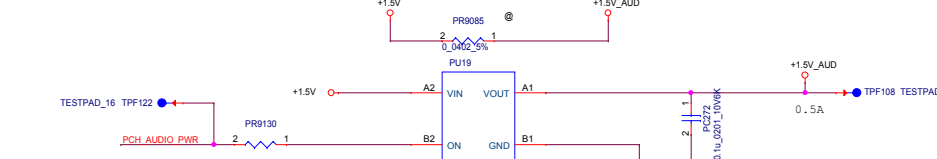
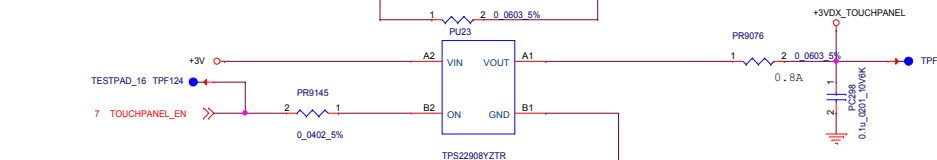
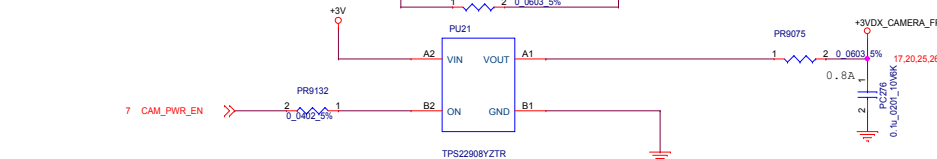
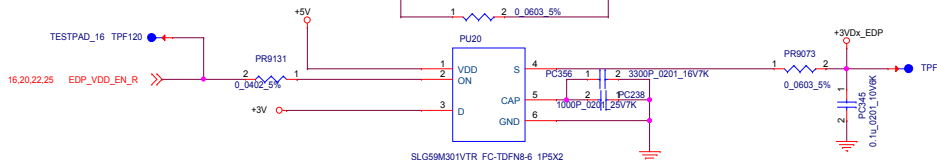
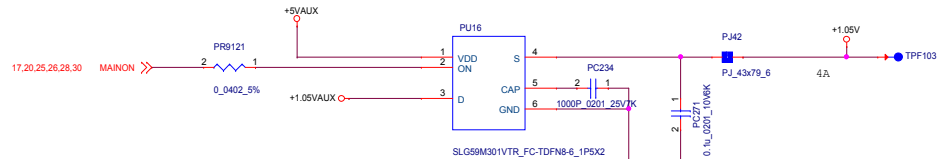
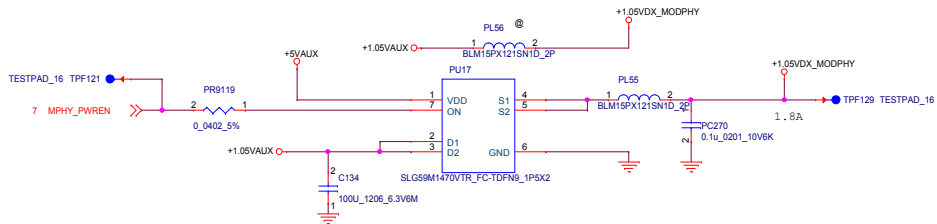
two more 0.1uF for EMC requant

PC161 and PC162 can place far from PL7

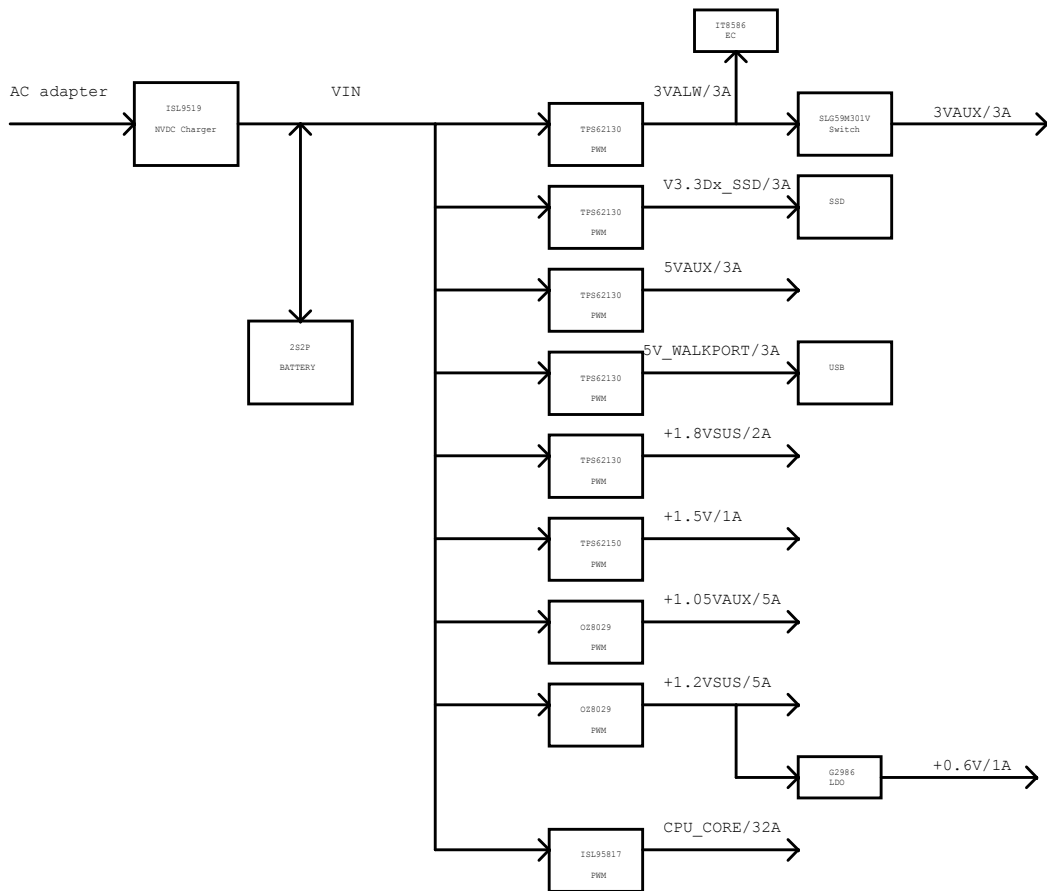
if AC input current>4.25A , pull low H_PROCHOT#
 if BAT input current>16.5A , pull low H_PROCHOT#

if BAT >7.4V cannot latch h-prochot#
 if VIM6.24V&BAT<7.4V latch h-prochot# low quickly

| | | |
|------|--------------------------|---------------|
| File | NVDC charger | |
| Rev | Document Number | Rev |
| D | 1 | V1.0 |
| Date | Tuesday, August 06, 2013 | Page 29 of 34 |



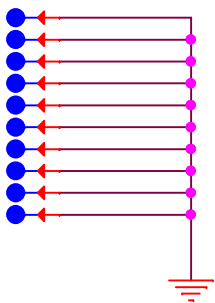
| | | | |
|-------|--------------------------|-------|--------------|
| Title | | | SWITCH POWER |
| Size | Document Number | Rev | |
| C | Kona | V1.0 | |
| Date: | Tuesday, August 06, 2013 | Sheet | 30 of 34 |



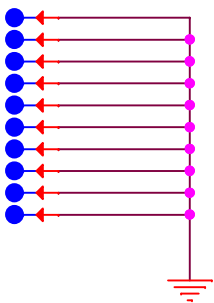
| | VCCRTC | VIN | +3VALW | +3VALW +5VALW +1.5VAUX | +1.35VSUS +0.68VSUS | +3V/+5V +1.5V/+1.05V +1.05_VCCST O280_VPE CPU_CORE |
|-----------|--------|-----|--------|------------------------------|------------------------|--|
| S0 | V | V | V | V | V | V |
| S3 | V | V | V | V | V | X |
| S4/S5(AC) | V | V | V | V | X | X |
| S4/S5(DC) | V | V | V | X | X | X |
| G3 | V | X | X | X | X | X |

| | SUSC# (SLP_S4) | SUSB# (SLP_S3) | +VALW | +VAUX | +VSUS | +V |
|-----------|-------------------|-------------------|-------|-------|-------|----|
| S0 | V | V | V | V | V | V |
| S3 | V | X | V | V | V | X |
| S4/S5(AC) | X | X | V | V | X | X |
| S4/S5(DC) | X | X | V | X | X | X |
| G3 | X | X | X | X | X | X |

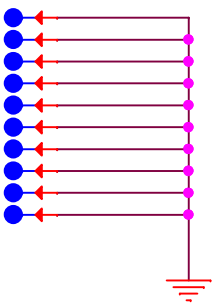
TESTPAD_16 TPF47
 TESTPAD_16 TPF46
 TESTPAD_16 TPF48
 TESTPAD_16 TPF50
 TESTPAD_16 TPF49
 TESTPAD_16 TPF51
 TESTPAD_16 TPF53
 TESTPAD_16 TPF52
 TESTPAD_16 TPF54
 TESTPAD_16 TPF55



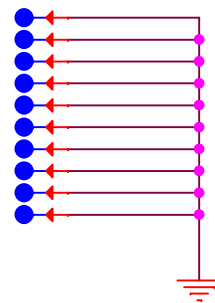
TESTPAD_16 TPF57
 TESTPAD_16 TPF56
 TESTPAD_16 TPF58
 TESTPAD_16 TPF60
 TESTPAD_16 TPF59
 TESTPAD_16 TPF61
 TESTPAD_16 TPF63
 TESTPAD_16 TPF62
 TESTPAD_16 TPF64
 TESTPAD_16 TPF65



TESTPAD_16 TPF67
 TESTPAD_16 TPF66
 TESTPAD_16 TPF68
 TESTPAD_16 TPF70
 TESTPAD_16 TPF69
 TESTPAD_16 TPF71
 TESTPAD_16 TPF73
 TESTPAD_16 TPF72
 TESTPAD_16 TPF74
 TESTPAD_16 TPF75

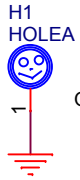


TESTPAD_16 TPF77
 TESTPAD_16 TPF76
 TESTPAD_16 TPF78
 TESTPAD_16 TPF80
 TESTPAD_16 TPF79
 TESTPAD_16 TPF81
 TESTPAD_16 TPF83
 TESTPAD_16 TPF82
 TESTPAD_16 TPF84
 TESTPAD_16 TPF85

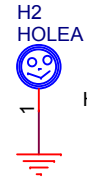


Put these 20 test point to TOP

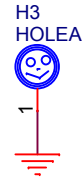
Put these 20 test point to Bottom



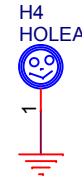
OVAL_Hole_22_55_SMD



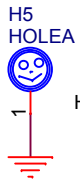
Hole_22_55_SMD_Half



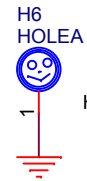
Hole_38_6_SMD



Hole_22_55_SMD



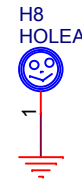
Hole_38_55_70_SMD



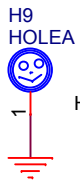
Hole_38_55_70_SMD



Hole_38_55_70_SMD



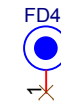
Hole_38_55_70_SMD



Hole_14_20_SMD



Hole_22_SMD



OVAL_Hole_14_18_SMD

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Title

Mounting Hole/EMI

Size
A

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